

Mixers, Modulators and Demodulators

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At base, radio communication involves translating information into radio form, letting it travel for a spell, and translating it back again. Translating information into radio form entails the process we call *modulation*, and *demodulation* is its reverse. One way or another, every transmitter used for radio communication, from the simplest to the most complex, includes a means of modulation; one way or another, every receiver used for radio communication, from the simplest to the most complex, includes a means of demodulation.

Modulation involves varying one or both of a radio signal's basic characteristics—amplitude and frequency (or phase)—to convey information. A circuit, stage or piece of hardware that modulates is called a *modulator*.

Demodulation involves reconstructing the transmitted information from the changing characteristic(s) of a modulated radio wave. A circuit, stage or piece of hardware that demodulates is called a *demodulator*.

Many radio transmitters, receivers and transceivers also contain *mixers*—circuits, stages or pieces of hardware that combine two or more signals to produce additional signals at sums of and differences between the original frequencies. Amateur Radio textbooks have traditionally handled mixers separately from modulators and demodulators, and modulators separately from demodulators.

This chapter, by David Newkirk, W9VES, and Rick Karlquist, N6RK, examines mixers, modulators and demodulators together because the job they do is essentially the same. Modulators and demodulators translate information into radio form and back again; mixers translate one frequency to others and back again. All of these translation processes can be thought of as forms of frequency translation or frequency shifting—the function traditionally ascribed to mixers. We'll therefore begin our investigation by examining what a mixer is (and isn't), and what a mixer does.

THE MECHANISM OF MIXERS AND MIXING

What is a Mixer?

Mixer is a traditional radio term for a circuit that shifts one signal's frequency up or down by combining it with another signal. The word *mixer* is also used to refer to a device used to blend multiple audio inputs together for recording, broadcast or sound reinforcement. These two mixer types differ in one very important way: A radio mixer makes new frequencies out of the frequencies put into it, and an audio mixer does not.

Mixing Versus Adding

Radio mixers might be more accurately called “frequency mixers” to distinguish them from devices such as “microphone mixers,” which are really just signal *combiners*, *summers* or *adders*. In their most basic, ideal forms, both devices have two inputs and one output. The combiner simply *adds* the instantaneous voltages of the two signals together to produce the output at each point in time (**Fig 15.1**). The mixer, on the other hand, *multiplies* the instantaneous voltages of the two signals together to produce its output signal from instant to instant (**Fig 15.2**). Comparing the output spectra of the combiner and mixer, we see that the combiner’s output contains only the frequencies of the two inputs, and nothing else, while the mixer’s output contains *new* frequencies. Because it combines one energy with another, this process is sometimes called *heterodyning*, from the Greek words for *other* and *power*.

Mixing as Multiplication

Since a mixer works by means of multiplication, a bit of math can show us how they work. To begin with, we need to represent the two signals we’ll mix, A and B, mathematically. Signal A’s instantaneous amplitude equals

$$A_a \sin 2\pi f_a t \quad (1)$$

in which A is peak amplitude, f is frequency, and t is time. Likewise, B’s instantaneous amplitude equals

$$A_b \sin 2\pi f_b t \quad (2)$$

Since our goal is to show that multiplying two signals generates sum and difference frequencies, we can simplify these signal definitions by assuming that the peak amplitude of each is 1. The equation for Signal A then becomes

$$a(t) = A \sin (2\pi f_a t) \quad (3)$$

and the equation for Signal B becomes

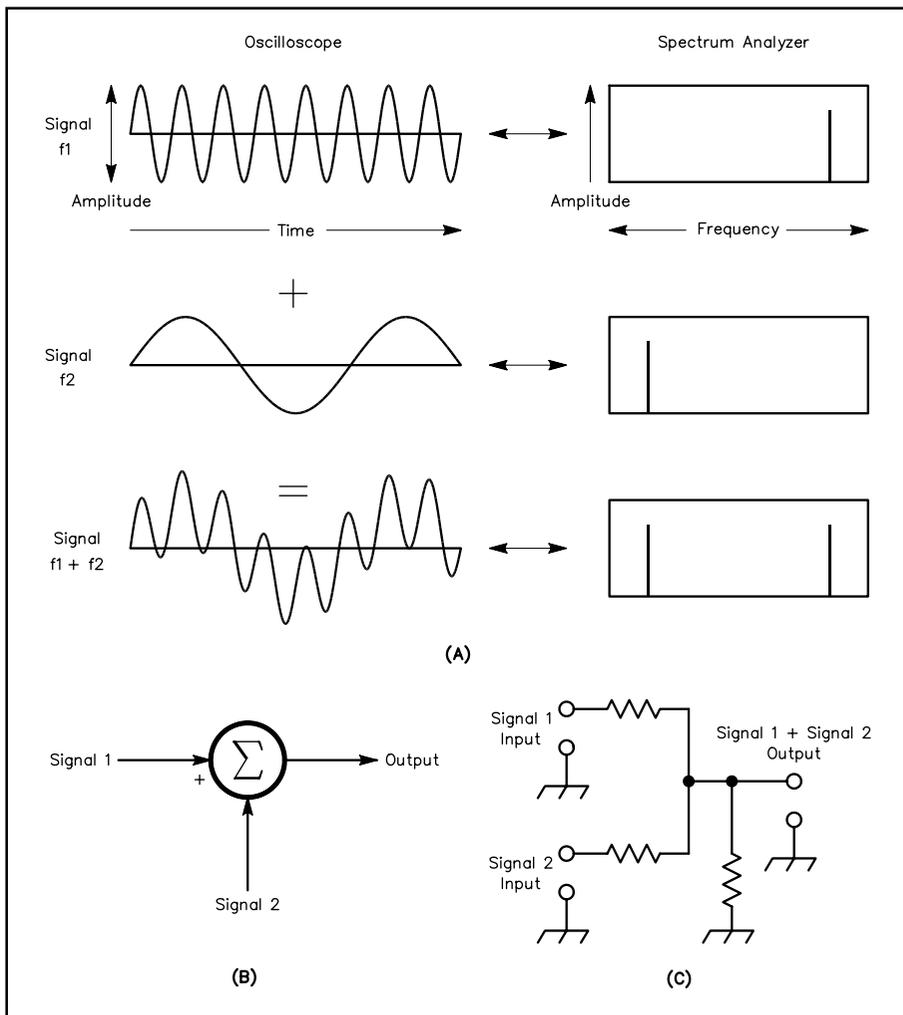


Fig 15.1—Adding or summing two sine waves of different frequencies (f1 and f2) combines their amplitudes without affecting their frequencies. Viewed with an *oscilloscope* (a real-time graph of amplitude versus time), adding two signals appears as a simple superimposition of one signal on the other. Viewed with a *spectrum analyzer* (a real-time graph of signal amplitude versus frequency), adding two signals just sums their spectra. The signals merely coexist on a single cable or wire. All frequencies that go into the adder come out of the adder, and no new signals are generated.

Drawing B, a block diagram of a summing circuit, emphasizes the stage’s mathematical operation rather than showing circuit components. Drawing C shows a simple summing circuit, such as might be used to combine signals from two microphones. In audio work, a circuit like this is often called a mixer—but it does not perform the same function as an RF mixer.

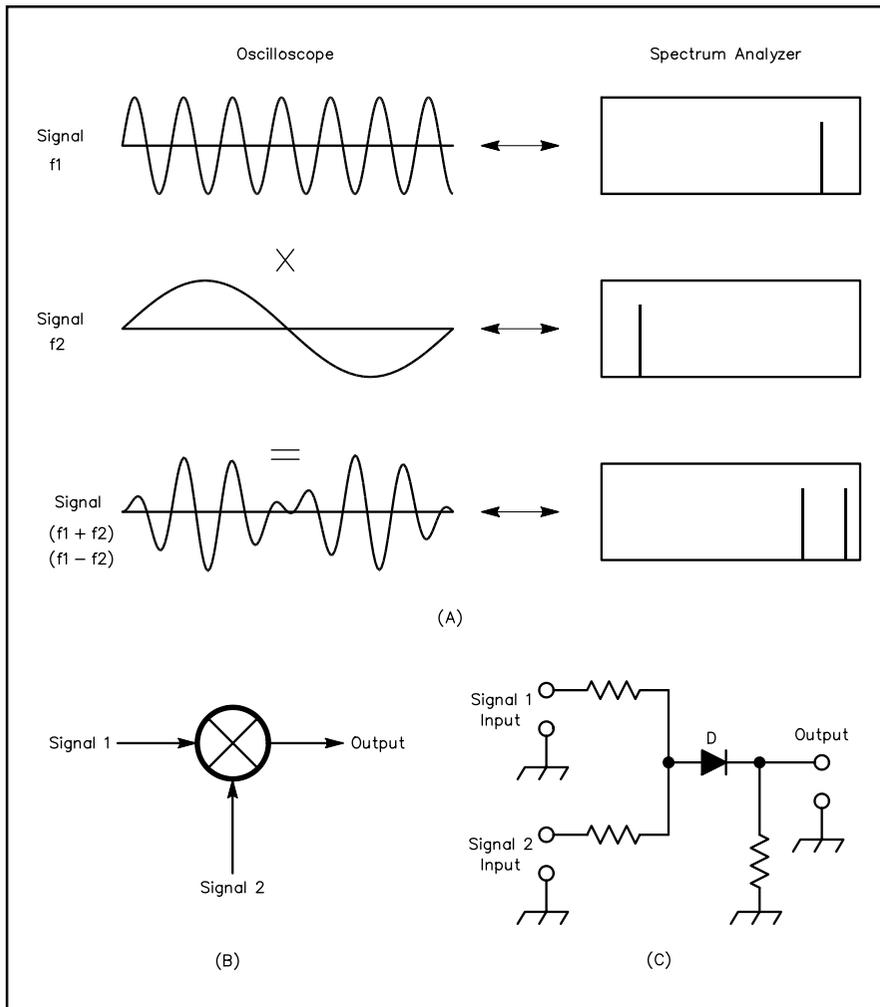


Fig 15.2—Multiplying two sine waves of different frequencies produces a new output spectrum. Viewed with an oscilloscope, the result of multiplying two signals is a composite wave that seems to have little in common with its components. A spectrum-analyzer view of the same wave reveals why: The original signals disappear entirely and are replaced by two new signals—at the *sum* and *difference* of the original signals' frequencies. Drawing B diagrams a multiplier, known in radio work as a mixer. The X emphasizes the stage's mathematical operation. (The circled X is only one of several symbols you may see used to represent mixers in block diagrams, as Fig 15.3 explains.)

Drawing C shows a very simple multiplier circuit. The diode, D, does the mixing. Because this circuit does other mathematical functions and adds them to the sum and difference products, its output is more complex than $f_1 + f_2$ and $f_1 - f_2$, but these can be extracted from the output by filtering.

$$b(t) = B \sin (2\pi f_b t) \quad (4)$$

Each of these equations represents a sine wave and includes a subscript letter to help us keep track of where the signals go.

Merely combining Signal A and Signal B by letting them travel on the same wire develops nothing new:

$$a(t) + b(t) = A \sin (2\pi f_a t) + B \sin (2\pi f_b t) \quad (5)$$

As needlessly reflexive as equation 5 may seem, we include it to highlight the fact that multiplying two signals is a quite different story. From trigonometry, we know that multiplying the sines of two variables can be expanded according to the relationship

$$\sin x \sin y = \frac{1}{2} [\cos(x - y) - \cos(x + y)] \quad (6)$$

Conveniently, Signals A and B are both sinusoidal, so we can use equation 6 to determine what happens when we multiply Signal A by Signal B. In our case, $x = 2\pi f_a t$ and $y = 2\pi f_b t$, so plugging them into equation 6 gives us

$$a(t) \cdot b(t) = \frac{AB}{2} \cos(2\pi[f_a - f_b]t) - \frac{AB}{2} \cos(2\pi[f_a + f_b]t) \quad (7)$$

Now we see two momentous results: a sine wave at the frequency *difference* between Signal A and Signal B $2\pi(f_a - f_b)t$, and a sine wave at the frequency *sum* of Signal A and Signal B $2\pi(f_a + f_b)t$. (The products are cosine waves, but since equivalent sine and cosine waves differ only by a phase shift of 90° , both are called *sine waves* by convention.)

This is the basic process by which we translate information into radio form and translate it back again. If we want to transmit a 1-kHz audio tone by radio, we can feed it into one of our mixer's inputs and feed an RF signal—say, 5995 kHz—into the mixer's other input. The result is two radio signals: one at 5994 kHz ($5995 - 1$) and another at 5996 kHz ($5995 + 1$). We have achieved modulation.

Converting these two radio signals back to audio is just as straightforward. All we do is feed them into one input of another mixer, and feed a 5995-kHz signal into the mixer's other input. Result: a 1-kHz tone. We have achieved demodulation; we have communicated by radio.

The key principle of a radio mixer is that, in mixing multiple signal voltages together, it adds and subtracts their frequencies to produce new frequencies. (In the field of signal processing, this process, *multiplication in the time domain*, is recognized as equivalent to the process of *convolution in the frequency domain*. Those interested in this alternative approach to describing the generation of new frequencies through mixing can find more information about it in the many textbooks available on this fascinating subject.) The difference between the mixer we've been describing and any mixer, modulator or demodulator that you'll ever use is that it's ideal. We put in two signals and got just two signals out. *Real* mixers, modulators and demodulators, on the other hand, also produce *distortion* products that make their output spectra “dirtier” or “less clean,” as well as putting out some energy at input-signal frequencies and their harmonics. Much of the art and science of making good use of multiplication in mixing, modulation and demodulation goes into minimizing these unwanted multiplication products (or their effects) and making multipliers do their frequency translations as efficiently as possible.

Putting Multiplication to Work

Piecing together a coherent picture of how multiplication works in radio communication isn't made any easier by the fact that traditional terms applied to a given multiplication approach and its products may vary with their application. If, for instance, you're familiar with standard textbook approaches to mixers, modulators and demodulators, you may be wondering why we didn't begin by working out the math involved by examining *amplitude modulation*, also known as *AM*. “Why not tell them about the *carrier* and how to get rid of it in a *balanced modulator*?” A transmitter enthusiast may ask “Why didn't you mention *sidebands* and how we conserve spectrum space and power by getting rid of one and putting all of our power into the other?” A student of radio receivers, on the other hand, expects any discussion of the same underlying multiplication issues to touch on the topics of *LO feedthrough*, *mixer balance* (*single* or *double*?), *image rejection* and so on.

You likely expect this book to spend some time talking to you about these things, so it will. But *this* radio-amateur-oriented discussion of mixers, modulators and demodulators will take a look at their common underlying mechanism *before* turning you loose on practical mixer, modulator and demodulator circuits. Then you'll be able to tell the forest from the trees. **Fig 15.3** shows the block symbol for a traditional mixer along with several IEC symbols for other functions mixers may perform.

It turns out that the mechanism underlying multiplication, mixing, modulation and demodulation is a pretty straightforward thing: Any circuit structure that *nonlinearly distorts* ac waveforms acts as a multiplier to some degree.

Nonlinear Distortion?

The phrase *nonlinear distortion* sounds redundant, but isn't. Distortion, an externally imposed change in a waveform, can be linear; that is, it can occur independently of signal amplitude. Consider a radio receiver front-end filter that passes only signals between 6 and 8 MHz. It does this by *linearly distorting*

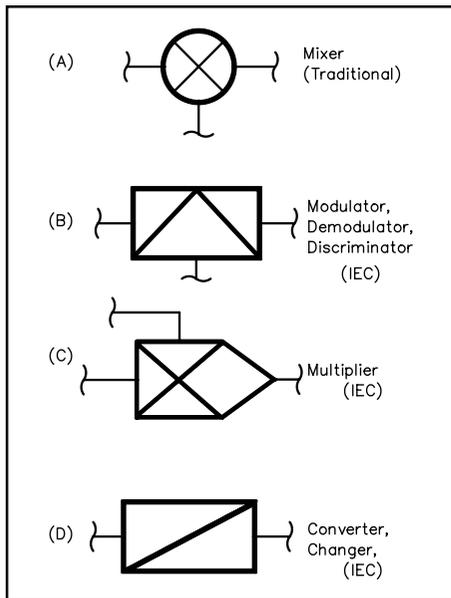


Fig 15.3—We commonly symbolize mixers with a circled X (A) out of tradition, but other standards sometimes prevail (B, C and D). Although the converter/changer symbol (D) can conceivably be used to indicate frequency changing through mixing, the three-terminal symbols are arguably better for this job because they convey the idea of two signal sources resulting in a new frequency. (IEC stands for International Electrotechnical Commission.)

the single complex waveform corresponding to the wide RF spectrum present at the radio's antenna terminals, reducing the amplitudes of frequency components below 6 MHz and above 8 MHz relative to those between 6 and 8 MHz. (Considering multiple signals on a wire as one complex waveform is just as valid, and sometimes handier, than considering them as separate signals. In this case, it's a bit easier to think of distortion as something that happens to a waveform rather than something that happens to separate signals relative to each other. It would be just as valid—and certainly more in keeping with the consensus view—to say merely that the filter attenuates signals at frequencies below 6 MHz and above 8 MHz.) The filter's output waveform certainly differs from its input waveform; the waveform has been distorted. But because this distortion occurs independently of signal level or polarity, the distortion is linear. No new frequency components are created; only the amplitude relationships among the wave's existing frequency components are altered. This is *amplitude* or *frequency* distortion, and all filters do it or they wouldn't be filters.

Phase or *delay distortion*, also linear, causes a complex signal's various component frequencies to be delayed by different amounts of time, depending on their frequency but independently of their amplitude. No new frequency components occur, and amplitude relationships among existing frequency components are not altered. Phase distortion occurs to some degree in all real filters.

The waveform of a nonsinusoidal signal can be changed by passing it through a circuit that has only linear distortion, but only *nonlinear distortion* can change the waveform of a simple sine wave. It can also produce an output signal whose output waveform changes as a function of the input amplitude, something not possible with linear distortion.

Nonlinear circuits often distort excessively with overly strong signals, but the distortion can be a complex function of the input level.

Nonlinear distortion may take the form of *harmonic distortion*, in which integer multiples of input frequencies occur, or *intermodulation distortion (IMD)*, in which different components multiply to make new ones.

Any departure from absolute linearity results in some form of nonlinear distortion, and this distortion can work for us or against us. Any so-called linear amplifier distorts nonlinearly to some degree; any device or circuit that distorts nonlinearly can work as a mixer, modulator, demodulator or frequency multiplier. An amplifier optimized for linear operation will nonetheless mix, but inefficiently; an amplifier biased for nonlinear amplification may be practically linear over a given tiny portion of its input-signal range. The trick is to use careful design and component selection to maximize nonlinear distortion when we want it, and minimize it when we don't. Once we've decided to maximize nonlinear distortion, the trick is to minimize the distortion products we don't want, and maximize the products we desire.

Keeping Unwanted Distortion Products Down

Ideally, a mixer multiplies the signal at one of its inputs by the signal at its other input, but does not multiply a signal at the same input by itself, or multiple signals at the same input by themselves or by each other. (Multiplying a signal by itself—squaring it—generates harmonic distortion [specifically,

second-harmonic distortion] by adding the signal’s frequency to itself per equation 7. Simultaneously squaring two or more signals generates simultaneous harmonic and intermodulation distortion, as we’ll see later when we explore how a diode demodulates AM.)

Consider what happens when a mixer must handle signals at two different frequencies (we’ll call them f_1 and f_2) applied to its first input, and a signal at a third frequency (f_3) applied to its other input. Ideally, a mixer multiplies f_1 by f_3 and f_2 by f_3 , but does not multiply f_1 and f_2 by each other. This produces output at the sum and difference of f_1 and f_3 , and the sum and difference of f_2 and f_3 , but *not* the sum and difference of f_1 and f_2 . **Fig 15.4** shows that feeding two signals into one input of a mixer results in the same output as if f_1 and f_2 are each first mixed with f_3 in two separate mixers, and the outputs of these mixers are combined. This shows that a mixer, even though constructed with nonlinearly distorting components, actually behaves as a *linear frequency shifter*. Traditionally, we refer to this process as mixing and to its outputs as *mixing products*, but we may also call it frequency *conversion*, referring to a device or circuit that does it as a *converter*, and to its outputs as *conversion products*.

Real mixers, however, at best act only as *reasonably* linear frequency shifters, generating some unwanted IMD products—spurious signals, or *spurs*—as they go. Receivers are especially sensitive to unwanted mixer IMD because the signal-level spread over which they must operate without generating unwanted IMD is often 90 dB or more, and includes infinitesimally weak signals in its span. In a receiver, IMD products so tiny that you’d never notice them in a transmitted signal can easily obliterate weak signals. This is why receiver designers apply so much effort to achieving “high dynamic range.”

The degree to which a given mixer, modulator or demodulator circuit produces unwanted IMD is often *the* reason why we use it, or don’t use it, instead of another circuit that does its wanted-IMD job as well or even better.

Other Mixer Outputs

In addition to desired sum-and-difference products and unwanted IMD products, real mixers also put out some energy at their input frequencies. Some mixer implementations may *suppress* these outputs—that is, reduce one or both of their input signals by a factor of 100 to 1,000,000, or 20 to 60 dB. This is good because it helps keep input signals at the desired mixer-output sum or difference frequency from showing up at the IF terminal—an effect reflected in a receiver’s *IF rejection* specification. Some mixer types, especially those used in the vacuum-tube era, suppress their input-signal outputs very little or not at all.

Input-signal suppression is part of an overall picture called *port-to-port isolation*. Mixer input and output connections are traditionally called *ports*. By tradition, the port to which we apply the shifting signal is the *local-oscillator (LO)* port. The convention for naming the other two ports (one of which must be an output, and the other of which must be an input) is usually that the higher-frequency port is called the *RF (radio frequency)* port and the lower-frequency port is called the *IF (intermediate frequency)* port. If a mixer’s output frequency is lower than its input frequency, then the RF port is an input and the IF port is an output. If the output frequency is higher than the input frequency, the IF port may be the input and the RF port may be the output. (We hedge with *may be* because usage varies. When in doubt, check a diagram carefully to determine which port is the “gozinta” and which port is the “gozouta.”)

It’s generally a good idea to keep a mixer’s input signals from

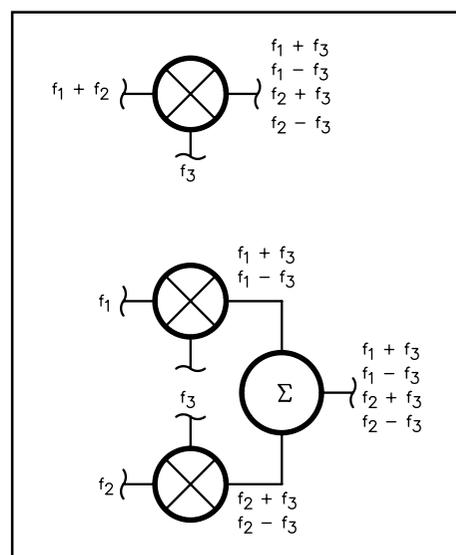


Fig 15.4—Feeding two signals into one input of a mixer results in the same output as if f_1 and f_2 are each first mixed with f_3 in two separate mixers, and the outputs of these mixers are combined.

appearing at its output port because they represent energy that we'd rather not pass on to subsequent circuitry. It therefore follows that it's usually a good idea to keep a mixer's LO-port energy from appearing at its RF port, or its RF-port energy from making it through to the IF port. But there are some notable exceptions.

Mixers and Amplitude Modulation

Now that we've just discussed what a fine thing it is to have a mixer that doesn't let its input signals through to its output port, we can explore a mixing approach that outputs one of its input signals so strongly that the fed-through signal's amplitude at least equals the combined amplitudes of the system's sum and difference products! This system, *amplitude modulation*, is the oldest means of translating information into radio form and back again. It's a frequency-shifting system in which the original unmodulated signal, traditionally called the *carrier*, emerges from the mixer along with the sum and difference products, traditionally called *sidebands*.

We can easily make the carrier pop out of our mixer along with the sidebands merely by building enough *dc level shift* into the information we want to mix so that its waveform never goes negative. Back at [equation 1 and 2](#), we decided to keep our mixer math relatively simple by setting the peak voltage of our mixer's input signals directly equal to their sine values. Each input signal's peak voltage therefore varies between +1 and -1, so all we need to do to keep our modulating-signal term (provided with a subscript m to reflect its role as the modulating or information waveform) from going negative is add 1 to it. Identifying the carrier term with a subscript c, we can write

$$\text{AM Signal} = (1 + m \sin 2\pi f_m t) \sin 2\pi f_c t \quad (8)$$

Notice that the modulation ($2\pi f_m t$) term has company in the form of a coefficient, m. This variable expresses the modulating signal's varying amplitude—variations that ultimately result in amplitude modulation. Expanding equation 8 according to [equation 6](#) gives us

$$\text{AM Signal} = \sin 2\pi f_c t + \frac{1}{2} m \cos(2\pi f_c - 2\pi f_m) t - \frac{1}{2} m \cos(2\pi f_c + 2\pi f_m) t \quad (9)$$

The modulator's output now includes the carrier ($\sin 2\pi f_c t$) in addition to sum and difference products that vary in strength according to m. According to the conventions of talking about modulation, we call the sum product, which comes out at a frequency higher than that of the carrier, the *upper sideband (USB)*, and the difference product, which comes out a frequency lower than that of the carrier, the *lower sideband (LSB)*. We have achieved amplitude modulation.

Why We Call It Amplitude Modulation

We call the modulation process described in equation 8 *amplitude modulation* because the complex waveform consisting of the sum of the sidebands and carrier varies with the information signal's magnitude (m). Concepts long used to illustrate AM's mechanism may mislead us into thinking that the *carrier* varies in strength with modulation, but careful study of equation 9 shows that this doesn't happen. The carrier, $\sin 2\pi f_c t$, goes into the modulator—we're in the modulation business now, so it's fitting to use the term *modulator* instead of *mixer*—as a sinusoid with an unvarying maximum value of |1|. The modulator multiplies the carrier by the dc level (+1) that we added to the information signal ($m \sin 2\pi f_m t$). Multiplying $\sin 2\pi f_c t$ by 1 merely returns $\sin 2\pi f_c t$. We have proven that the carrier's amplitude does not vary as a result of amplitude modulation. The carrier is, however, used by many circuits as a reference signal.

Overmodulation

Since an AM signal's information content resides entirely in its sidebands, it follows that the more

energetic we make the sidebands, the more information energy will be available for an AM receiver to “recover” when it demodulates the signal. Even in an ideal modulator, there’s a practical limit to how strong we can make an AM signal’s sidebands relative to its carrier, however. Beyond that limit, we severely distort the waveform we want to translate into radio form.

We reach AM’s distortion-free modulation limit when the sum of the sidebands and carrier at the modulator output *just reaches zero* at the modulating wave-form’s most negative peak (**Fig 15.5**). We call this condition *100% modulation*, and it occurs when m equals 1. (We enumerate *modulation percentage* in values from 0 to 100%. The lower the number, the less information energy in the sidebands. You may also see modulation enumerated in terms of a *modulation factor* from 0 to 1, which directly equals m ; a modulation factor of 1 is the same as 100% modulation.) **Equation 9** shows that each sideband’s voltage is half that of the carrier. Power varies as the square of voltage, so the power in each sideband of a 100%-modulated signal is therefore $(1/2)^2$ times, or 1/4, that of the carrier. A transmitter capable of 100% modulation when operating at a carrier power of 100 W therefore puts out a 150-W signal at 100% modulation, 50 W of which is attributable to the sidebands. (The *peak envelope power [PEP]* output of a double-sideband, full-carrier AM transmitter at 100% modulation is four times its carrier PEP. This is why our solid-state, “100-W” MF/HF transceivers are usually rated for no more than about 25 W carrier output at 100% amplitude modulation.)

One-hundred-percent modulation is a brick-wall limit because an amplitude modulator can’t reduce its output to less than zero. Trying to increase modulation beyond the 100% point results in *overmodulation* (**Fig 15.6**), in which the modulation envelope no longer mirrors the shape of the modulating wave (**Fig 15.6A**). An overmodulated wave contains more energy than it did at 100% modulation, but some of the added energy now exists as *harmonics of the modulating waveform* (**Fig 15.6B**). This distortion makes the modulated signal take up more spectrum space than it needs. In voice operation, overmodulation commonly happens only on

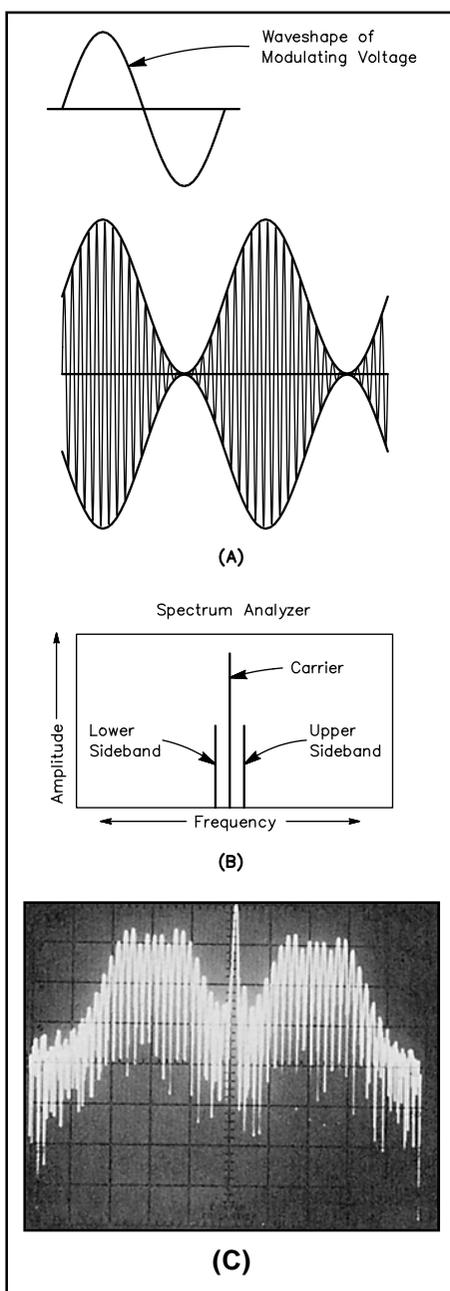


Fig 15.5—Graphed in terms of amplitude versus time (A), the envelope of a properly modulated AM signal exactly mirrors the shape of its modulating waveform, which is a sine wave in this example. This AM signal is modulated as fully as it can be—100%—because its envelope *just hits zero* on the modulating wave’s negative peaks. Graphing the same AM signal in terms of amplitude versus frequency (B) reveals its three spectral components: Carrier, upper sideband and lower sideband. B shows sidebands as single-frequency components because the modulating waveform is a sine wave. With a complex modulating waveform, the modulator’s sum and difference products really do show up as *bands* on either side of the carrier (C).

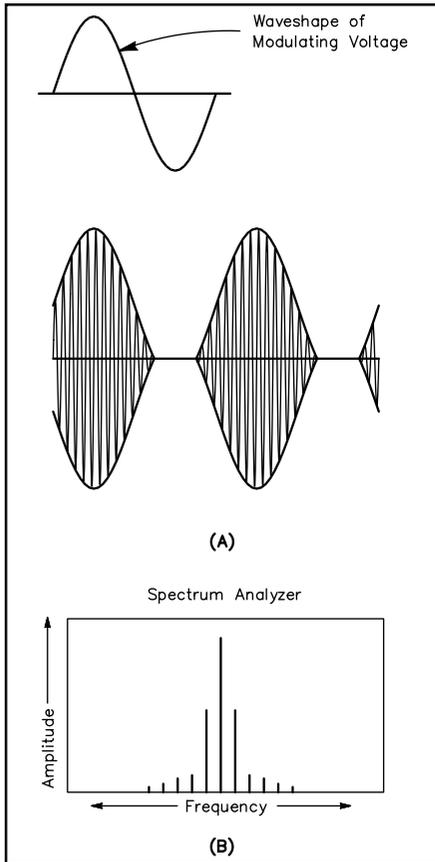


Fig 15.6—Overmodulating an AM transmitter results in a modulation envelope (A) that doesn't faithfully mirror the modulating waveform. This distortion creates additional sideband components that broaden the transmitted signal (B).

syllabic peaks, making the distortion products sound like crashy, transient noise we refer to as *splatter*.

Modulation Linearity

If we increase an amplitude modulator's modulating-signal input by a given percentage, we expect a proportional modulation increase in the modulated signal. We expect good *modulation linearity*. Suboptimal amplitude modulator design may not allow this,

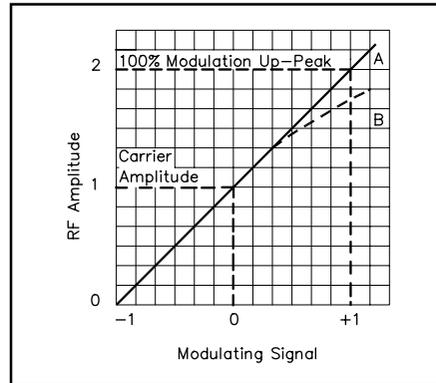


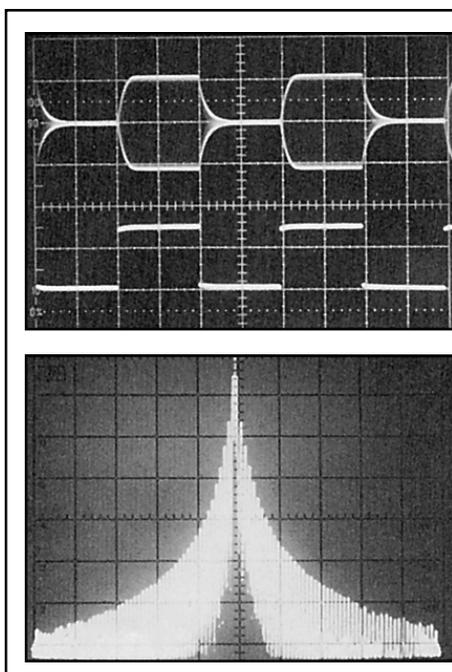
Fig 15.7—An ideal AM transmitter exhibits a straight-line relationship (A) between its instantaneous envelope amplitude and the instantaneous amplitude of its modulating signal. Distortion, and thus an unnecessarily wide signal, results if the transmitter cannot respond linearly across the modulating signal's full amplitude range.

however. Above some modulation percentage, a modulator may fail to increase modulation in proportion to an increase its input signal (**Fig 15.7**). Distortion, and thus an unnecessarily wide signal, results.

Using AM to Send Morse Code

Fig 15.6A closely resembles what we see when a properly adjusted CW transmitter sends a string of dots (**Fig 15.8**). Keying a carrier on and off produces a wave that varies in amplitude and has double (upper and lower) sidebands that vary in spectral composition according to the duration and envelope shape of the on-off transitions. The emission mode we call *CW* is therefore a form of AM. The concepts of modulation percentage and overmodulation are usually not applied to generating an on-off-keyed Morse signal, however. This is related to how we copy CW by ear, and the fact that, in CW radio communication, we usually don't translate the received signal all the way back into its original pre-modulator (*baseband*) form, as a closer look at the process reveals.

In CW transmission, we usually open and close a keying line to make dc transitions that



(A) Fig 15.8—Telegraphy by on-off-keying a carrier is also a form of AM, called CW (short for *continuous wave*) for reasons of tradition. *Waveshaping* in a CW transmitter often causes a CW signal's RF envelope (the amplitude-versus-time display at A) to contain less harmonic energy than the faster transitions of its keying waveform (B) suggest should be the case. C, an amplitude-versus-frequency display, shows that even a properly shaped CW signal has many sideband components.

turn the transmitted carrier on and off. CW reception usually does not entirely reverse this process, however. Instead of demodulating a CW signal all the way back to its baseband self—a shifting dc level—we want the presences and absences of its carrier to create long and short audio tones. Because the carrier is RF and not AF, we must mix it with a locally generated RF signal—from a *beat-frequency oscillator (BFO)*—that’s close enough in frequency to produce a difference signal at AF. What goes into our transmitter as shifting dc comes out of our receiver as thump-delimited tone bursts of dot and dash duration. We have achieved CW communication.

The dots and dashes of a CW signal must start and stop abruptly enough so we can clearly distinguish the carrier’s presences and absences from noise, especially when fading prevails. The keying sidebands, which sound like little more than thumps when listened to on their own, help our brains be sure when the carrier tone starts and stops.

It so happens that we always need to hear one or more harmonics of the fundamental keying waveform for the code to sound sufficiently crisp. If the transmitted signal will be subject to propagational fading—a safe assumption for any long-distance radio communication—we *harden* our keying by making the transmitter’s output rise and fall more quickly. This puts more energy into more keying sidebands and makes the signal more copiable in the presence of fading—in particular, *selective fading*, which linearly distorts a modulated signal’s complex waveform and randomly changes the sidebands’ strength and phase relative to the carrier and each other. The appropriate keying hardness also depends on the keying speed. The faster the keying in WPM, the faster the on-off times—the harder the keying—must be for the signal to remain ear-readable through noise and fading.

Instead of thinking of this process in terms of modulation percentage, we just ensure that a CW transmitter produces sufficient keying-sideband energy for solid reception. Practical CW transmitters rarely do their keying with a modulator stage as such. Instead, one or more stages are turned on and off to modulate the carrier with Morse, with rise and fall times set by R and C values associated with the stages’ keying and/or power supply lines. A transmitter’s CW *waveshaping* is therefore usually hardwired to values appropriate for reasonably high-speed sending (35 to 55 WPM or so) in the presence of fading. As a result, we generally cannot vary keying hardness at will as we might vary a voice transmitter’s modulation with a front-panel control. Rise and fall times of 1 to 5 ms (5 ms rise and fall times equate to a keying speed of 36 WPM in the presence of fading and 60 WPM if fading is absent) are common.

The faster a CW transmitter’s output changes between zero and maximum, the more bandwidth its carrier and sidebands occupy. Making a CW signal’s keying too hard is therefore spectrum-wasteful and unneighborly because it makes the signal wider than it needs to be. Keying sidebands that are stronger and wider than necessary are traditionally called *clicks* because of what they sound like on the air. There is a more detailed discussion of keying waveforms in the [Receivers, Transmitters, Transceivers](#) chapter.

The Many Faces of Amplitude Modulation

We’ve so far examined mixers, multipliers and modulators that produce complex output signals of two types. One, the action of which [equation 7](#) expresses, produces only the frequency sum of and frequency difference between its input signals. The other, the amplitude modulator characterized by [equations 8 and 9](#), produces carrier output in addition to the frequency sum of and frequency difference between its input signals. Exploring the AM process led us to a discussion on-off-keyed CW, which is also a form of AM.

Amplitude modulation is nothing more and nothing less than varying an output signal’s amplitude according to a varying voltage or current. All of the output signal types mentioned above are forms of amplitude modulation, and there are others. Their names and applications depend on whether the resulting signal contains a carrier or not, and both sidebands or not. Here’s a brief overview of AM-signal types, what they’re called, and some of the jobs you may find them doing:

- *Double-sideband (DSB), full-carrier AM* is often called just *AM*, and often what’s meant when radio

folk talk about just *AM*. (When the subject is broadcasting, *AM* can also refer to broadcasters operating in the 525- to 1705-kHz region, generically called *the AM band* or *the broadcast band* or *medium wave*. These broadcasters used only double-sideband, full-carrier AM for many years, but many now use combinations of amplitude modulation and *angle modulation*, which we'll explore shortly, to transmit stereophonic sound.) Equations 8 and 9 express what goes on in generating this signal type. What we call *CW*—Morse code done by turning a carrier on and off—is a form of DSB, full-carrier AM.

- *Double-sideband, suppressed-carrier AM* is what comes out of a circuit that does what equation 7 expresses—a sum (upper sideband), a difference (lower sideband) and no carrier. We didn't call its sum and difference outputs upper and lower sidebands earlier in equation 7's neighborhood, but we'd do so in a transmitting application. In a transmitter, we call a circuit that suppresses the carrier while generating upper and lower sidebands a *balanced modulator*, and we quantify its *carrier suppression*, which is always less than infinite. In a receiver, we call such a circuit a *balanced mixer*, which may be *single-balanced* (if it lets either its RF signal or its LO [carrier] signal through to its output) or *double-balanced* (if it suppresses both its input signal and LO/carrier in its output), and we quantify its *LO suppression* and *port-to-port isolation*, which are always less than infinite. (Mixers [and amplifiers] that afford no balance whatsoever are sometimes said to be *single-ended*.) Sometimes, DSB suppressed-carrier AM is called just *DSB*.
- *Vestigial sideband (VSB), full-carrier AM* is like the DSB variety with one sideband partially filtered away for bandwidth reduction. Commercial television systems that transmit AM video use VSB AM.
- *Single-sideband, suppressed-carrier AM* is what you get when you generate a DSB, suppressed carrier AM signal and throw away one sideband with filtering or phasing. We usually call this signal type just *single sideband (SSB)* or, as appropriate, *upper sideband (USB)* or *lower sideband (LSB)*. In a modulator or demodulator system, the *unwanted sideband*—that is, the sum or difference signal we don't want—may be called just that, or it may be called the *opposite sideband*, and we refer to a system's *sideband rejection* as a measure of how well the opposite sideband is suppressed. In receiver mixers not used for demodulation and transmitter mixers not used for modulation, the unwanted sum or difference signal, or the input signal that produces the unwanted sum or difference, is the *image*, and we refer to a system's *image rejection*. A pair of mixers specially configured to suppress either the sum or the difference output is an *image-reject mixer (IRM)*. In receiver demodulators, the unwanted sum or difference signal may just be called the opposite sideband, or it may be called the *audio image*. A receiver capable of rejecting the opposite sideband or audio image is said to be capable of *single-signal* reception.
- *Single-sideband, full-carrier AM* is akin to full-carrier DSB with one sideband missing. Commercial and military communicators may call it *AM equivalent (AME)* or *compatible AM (CAM)*—*compatible* because it can be usefully demodulated in AM and SSB receivers and because it occupies about the same amount of spectrum space as SSB.)
- *Independent sideband (ISB) AM* consists an upper sideband and a lower sideband containing different information (a carrier of some level may also be present). Radio amateurs sometimes use ISB to transmit simultaneous slow-scan-television and voice information; international broadcasters sometimes use it for point-to-point audio feeds as a backup to satellite links.

Mixers and AM Demodulation

Translating information from radio form back into its original form—demodulation—is also traditionally called *detection*. If the information signal we want to detect consists merely of a baseband signal frequency-shifted into the radio realm, almost any low-distortion frequency-shifter that works according to equation 7 can do the job acceptably well.

Sometimes we recover a radio signal's information by shifting the signal right back to its original form with no intermediate frequency shifts. This process is called *direct conversion*. More commonly, we first

convert a received signal to an *intermediate frequency* so we can amplify, filter and level-control it prior to detection. This is *superheterodyne* reception, and most modern radio receivers work in this way. Whatever the receiver type, however, the received signal ultimately makes its way to one last mixer or demodulator that completes the final translation of information back into audio, or into a signal form suitable for device control or computer processing. In this last translation, the incoming signal is converted back to recovered-information form by mixing it with one last RF signal. In heterodyne or *product* detection, that final frequency-shifting signal comes from a BFO. The incoming-signal energy goes into one mixer input port, BFO energy goes into the other, and audio (or whatever form the desired information takes) results.

If the incoming signal is full-carrier AM and we don't need to hear the carrier as a tone, we can modify this process somewhat, if we want. We can use the carrier itself to provide the heterodyning energy in a process called *envelope detection*.

Envelope Detection and Full-Carrier AM

Fig 15.5 graphically represents how a full-carrier AM signal's *modulation envelope* corresponds to the shape of the modulating wave. If we can derive from the modulated signal a voltage that varies according to the modulation envelope, we will have successfully recovered the information present in the sidebands. This process is called envelope detection, and we can achieve it by doing nothing more complicated than half-wave-rectifying the modulated signal with a diode (Fig 15.9).

That a diode demodulates an AM signal by allowing its carrier to multiply with its sidebands may jar those long accustomed to seeing diode detection ascribed merely to "rectification." But a diode is certainly nonlinear. It passes current in only one direction, and its output voltage is (within limits) proportional to the square of its input voltage. These nonlinearities allow it to multiply.

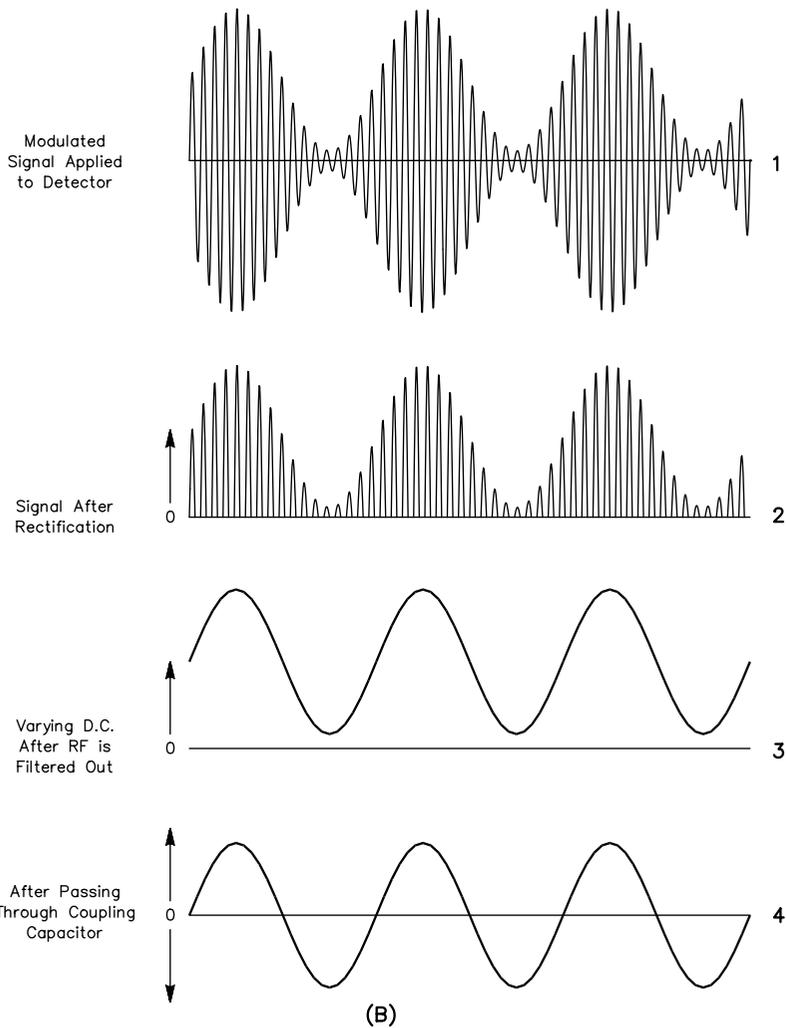
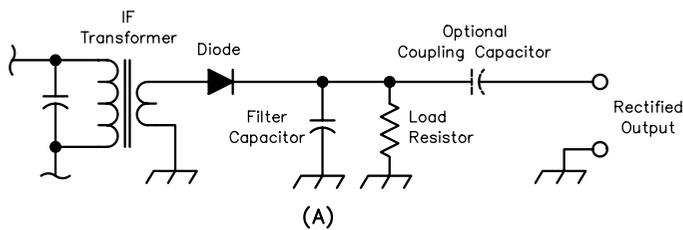
Exploring this mathematically is tedious with full-carrier AM because the process squares three summed components (carrier, lower sideband and upper sideband). Rather than fill the better part of a page with algebra, we'll instead characterize the outcome verbally: In "just rectifying" a DSB, full-carrier AM signal, a diode detector produces

- Direct current (the result of rectifying the carrier);
- A second harmonic of the carrier;
- A second harmonic of the lower sideband;
- A second harmonic of the upper sideband;
- Two difference-frequency outputs (upper sideband minus carrier, carrier minus lower sideband), each of which is equivalent to the modulating waveform's frequency, and both of which sum to produce the recovered information signal; and
- A second harmonic of the modulating waveform (the frequency difference between the two sidebands).

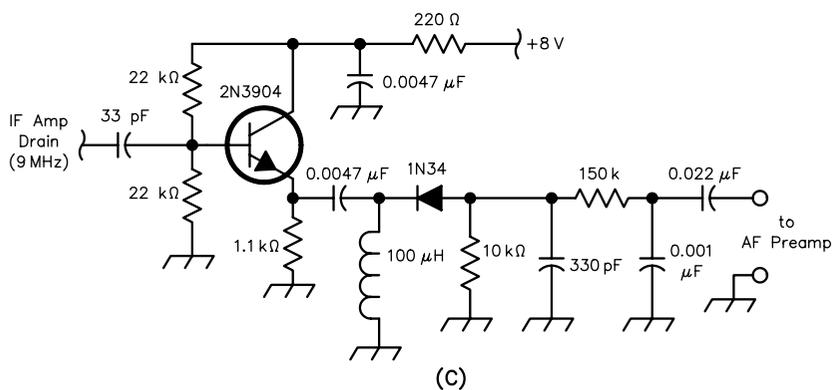
Three of these products are RF. Low-pass filtering, sometimes little more than a simple RC network, can remove the RF products from the detector output. A capacitor in series with the detector output line can block the carrier-derived dc component. That done, only two signals remain: the recovered modulation and, at a lower level, its second harmonic—in other words, second-harmonic distortion of the desired information signal.

Mixers and Angle Modulation

Amplitude modulation served as our first means of translating information into radio form because it could be implemented as simply as turning an electric noise generator on and off. (A spark transmitter consisted of little more than this.) By the 1930s, we had begun experimenting with translating information into radio form and back again by modulating a radio wave's angular velocity (frequency or phase) instead of its overall amplitude. The result of this process is *frequency modulation (FM)* or *phase*



(B)



(C)

Fig 15.9—Radio's simplest demodulator, the diode rectifier (A), demodulates an AM signal by multiplying its carrier and sidebands to produce frequency sums and differences, two of which sum into a replica of the original modulation (B). Modern receivers often use an emitter follower to provide low-impedance drive for their diode detectors (C).

modulation (PM), both of which are often grouped under the name *angle modulation* because of their underlying principle.

A change in a carrier's frequency or phase for the purpose of modulation is called *deviation*. An FM signal deviates according to the amplitude of its modulating waveform, independently of the modulating waveform's frequency; the higher the modulating wave's amplitude, the greater the deviation. A PM signal deviates according to the amplitude *and frequency* of its modulating waveform; the higher the modulating wave's amplitude *and/or frequency*, the greater the deviation.

An angle-modulated signal can be mathematically represented as

$$\begin{aligned} f_c(t) &= \cos(2\pi f_c t + m \sin(2\pi f_m t)) \\ &= \cos(2\pi f_c t) \cos(m \sin(2\pi f_m t)) \\ &= \sin(2\pi f_c t) \sin(m \sin(2\pi f_m t)) \end{aligned} \tag{10}$$

In it, we see the carrier frequency ($2\pi f_c t$) and modulating signal ($\sin 2\pi f_m t$) as in the equation for AM (equation 8). We again see the modulating signal associated with a coefficient, m , that relates to degree of modulation. (In the AM equation, m is the modulation factor; in the angle-modulation equation, m is the *modulation index* and, for FM, equals the deviation divided by the modulating frequency.) We see that angle-modulation occurs as the cosine of the sum of the carrier frequency ($2\pi f_c t$) and the modulating signal ($\sin 2\pi f_m t$) times the modulation index (m). In its expanded form, we see the appearance of sidebands above and below the carrier frequency.

Angle modulation is a multiplicative process, so, like AM, it creates sidebands on both sides of the carrier. Unlike AM, however, angle modulation creates an *infinite* number of sidebands on either side of the carrier! This occurs as a direct result of modulating the carrier's angular velocity, to which its frequency and phase directly relate. If we continuously vary a wave's angular velocity according to another periodic wave's cyclical amplitude variations, the rate at which the modulated wave repeats *its* cycle—its frequency—passes through an infinite number of values. (How many individual amplitude points are there in one cycle of the modulating wave? An infinite number. How many corresponding discrete frequency or phase values does the corresponding angle-modulated wave pass through as the modulating signal completes a cycle? An infinite number!) In AM, the carrier frequency stays at one value, so AM produces two sidebands—the sum of its carrier's unchanging frequency value and the modulating frequency, and the difference between the carrier's unchanging frequency value and the modulating frequency. In angle modulation, the modulating wave shifts the frequency or phase of the carrier through an infinite number of different frequency or phase values, resulting in an infinite number of sum and difference products.

Wouldn't the appearance on the air of just a few such signals result in a bedlam of mutual interference? No, because most of angle modulation's uncountable sum and difference products are vanishingly weak in practical systems, and because they don't show up just anywhere in the spectrum. Rather, they emerge from the modulator spaced from the average ("resting," unmodulated) carrier frequency by integer multiples of the modulating frequency (Fig 15.10). The strength of the sidebands relative to the carrier, and the strength and phase of the carrier itself, vary with the degree of modulation—the modulation index. (The *overall* amplitude of an angle-modulated signal does not change with modulation, however; when energy goes out of the carrier, it shows up in the sidebands, and vice versa.) In practice, we operate angle-modulated transmitters at modulation indexes that make all but a few of their infinite sidebands small in amplitude. (A mathematical tool called *Bessel functions*, part of the set of which is shown graphically in the **Modulation Sources** chapter, lets us determine the relative strength of the carrier and sidebands according to modulation index.) Selectivity in transmitter and receiver circuitry further modify this relationship, especially for sidebands far away from the carrier.

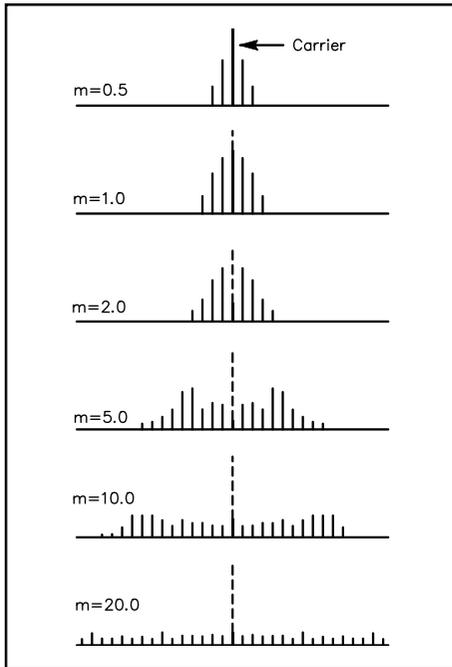


Fig 15.10—Angle-modulation produces a carrier and an infinite number of upper and lower sidebands spaced from the average (“resting,” unmodulated) carrier frequency by integer multiples of the modulating frequency. (This drawing is a simplification because it only shows relatively strong, close-in sideband pairs; space constraints prevent us from extending it to infinity.) The relative amplitudes of the sideband pairs and carrier vary with modulation index, m .

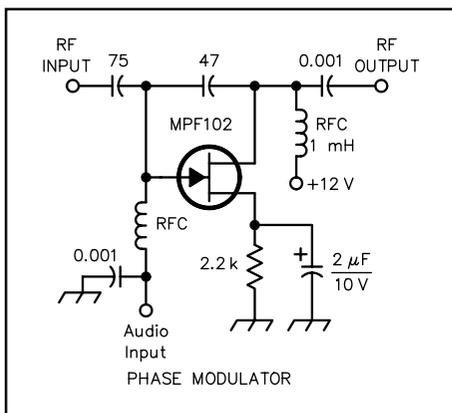


Fig 15.12—A series reactance modulator acts as a variable shunt around a reactance—in this case, a 47-pF capacitor—through which the carrier passes.

Angle Modulators

Vary a reactance in or associated with an oscillator’s frequency-determining element(s), and you vary the oscillator’s frequency. Vary the tuning of a tuned circuit through which a signal passes, and you vary the signal’s phase. A circuit that does this is called a *reactance modulator*, and can be little more than a tuning diode or two connected to a tuned circuit in an oscillator or amplifier (**Fig 15.11**). Varying a reactance through which the signal passes (**Fig 15.12**) is another way of doing the same thing.

The difference between FM and PM depends solely on how, and not how much, deviation occurs. A modulator that causes deviation in proportion to the modulating wave’s amplitude and frequency is a phase modulator. A modulator that causes deviation only in proportion to the modulating signal’s amplitude is a frequency modulator.

Increasing Deviation by Frequency Multiplication

Maintaining modulation linearity is just as important in angle modulation as it is in AM, because unwanted distortion is always our enemy. A given angle-modulator circuit can frequency- or phase-shift a carrier only so much before the shift stops occurring in strict proportion to the amplitude (or, in PM, the amplitude and frequency) of the modulating signal.

If we want more deviation than an angle modulator can linearly

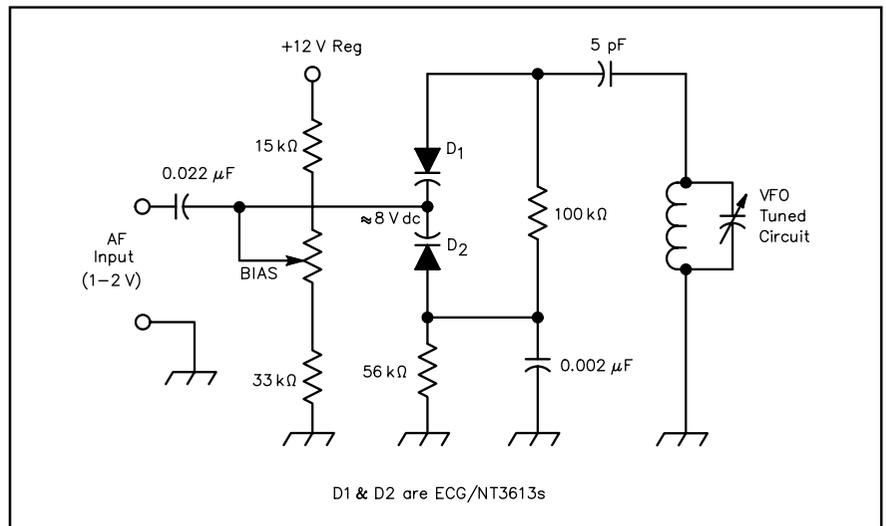


Fig 15.11—One or more tuning diodes can serve as the variable reactance in a reactance modulator. This HF reactance modulator circuit uses two diodes in series to ensure that the tuned circuit’s RF-voltage swing cannot bias the diodes into conduction. D1 and D2 are “30-volt” tuning diodes that exhibit a capacitance of 22 pF at a bias voltage of 4. The BIAS control sets the point on the diode’s voltage-versus-capacitance characteristic around which the modulating waveform swings.

achieve, we can operate the modulator at a suitable subharmonic—submultiple—of the desired frequency, and process the modulated signal through a series of *frequency multipliers* to bring it up to the desired frequency. The deviation also increases by the overall multiplication factor, relieving the modulator of having to do it all directly. A given FM or PM radio design may achieve its final output frequency through a combination of mixing (frequency shift, no deviation change) and frequency multiplication (frequency shift *and* deviation change).

The Truth About “True FM”

Something we covered a bit earlier bears closer study:

An FM signal deviates according to the amplitude of its modulating waveform, independently of the modulating waveform’s frequency; the higher the modulating wave’s amplitude, the greater the deviation. A PM signal deviates according to the amplitude *and frequency* of its modulating waveform; the higher the modulating wave’s amplitude *and/or frequency*, the greater the deviation.

The practical upshot of this excerpt is that we can use a phase modulator to generate FM. All we need to do is run a PM transmitter’s modulating signal through a low-pass filter that (ideally) halves the signal’s amplitude for each doubling of frequency (a reduction of “6 dB per octave,” as we sometimes see such responses characterized) to compensate for its phase modulator’s “more deviation with higher frequencies” characteristic. The result is an FM, not PM, signal. FM achieved with a phase modulator is sometimes called *indirect FM* as opposed to the *direct FM* we get from a frequency modulator.

We sometimes see radio gear manufacturers claim that one piece of gear is better than another solely because it generates “true FM” as opposed to indirect FM. We can immunize ourselves against such claims by keeping in mind that direct and indirect FM *sound exactly alike in a receiver* when done correctly.

Depending on the nature of the modulation source, there *is* a practical difference between a frequency modulator and a phase modulator. Answering two questions can tell us whether this difference matters: Does our modulating signal contain a dc level or not? If so, do we need to accurately preserve that dc level through our radio communication link for successful communication? If both answers are *yes*, we must choose our hardware and/or information-encoding approach carefully, because a frequency modulator can convey shifts in its modulating wave’s dc level, while a phase modulator, which responds only to instantaneous changes in frequency and phase, cannot.

Consider what happens when we want to frequency-modulate a phase-locked-loop-synthesized transmitted signal. **Fig 15.13** block-diagrams a PLL frequency modulator. Normally, we modulate a PLL’s VCO because it’s the easy thing to do. As long as our modulating frequency results in frequency excursions too fast for the PLL to follow and correct—that is, as long as our modulating frequency is outside the PLL’s *loop bandwidth*—we achieve the FM we seek. Trying to modulate a dc level by pushing the VCO to a particular frequency and holding it there fails, however, because a PLL’s loop response includes dc. The loop sees the modulation’s dc component as a correctable error and dutifully “fixes” it. FModing a PLL’s VCO therefore can’t buy us the dc response “true FM” is supposed to allow.

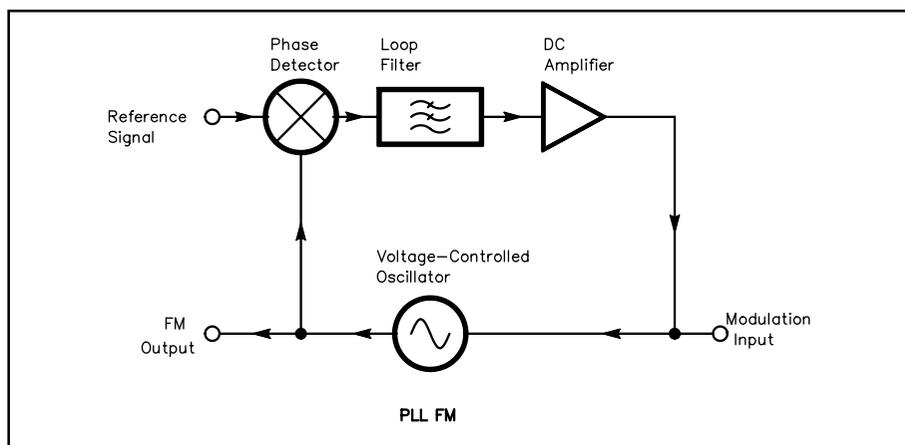


Fig 15.13—Frequency modulation, PLL-style.

We can dc-modulate a PLL modulator, but we must do so by modulating the loop's *reference*. The PLL then adjusts the VCO to adapt to the changed reference, and our dc level gets through. In this case, the modulating frequency must be *within* the loop bandwidth—which dc certainly is—or the VCO won't be corrected to track the shift.

Mixers and Angle Demodulation

With the awesome prospect of generating an infinite number of sidebands still fresh in our minds, we may be a bit disappointed to learn that we commonly demodulate angle modulation by doing little more than turning it into AM and then envelope- or product-detecting it! But this is what happens in many of our FM receivers and transceivers, and we can get a handle on this process by realizing that a form of angle-modulation-to-AM conversion begins quite early in an angle-modulated signal's life because of distortion of the modulation by amplitude-linear circuitry—something that happens to angle-modulated signals, it turns out, in any linear circuit that doesn't have an amplitude-versus-frequency response that's utterly flat out to infinity.

Think of what happens, for example, when we sweep a constant-amplitude signal up in frequency—say, from 1 kHz to 8 kHz—and pass it through a 6-dB-per-octave filter (Fig 15.14A). The filter's rolloff causes the output signal's amplitude to decrease as frequency increases. Now imagine that we linearly sweep our constant-amplitude signal *back and forth* between 1 kHz and 8 kHz at a constant rate of 3 kHz per second (Fig 15.14B). The filter's output *amplitude* now varies cyclically over time as the input signal's *frequency* varies cyclically over time. Right before our eyes, a frequency change turns into an amplitude change. The process of converting angle modulation to amplitude modulation has begun.

This is what happens whenever an angle-modulated signal passes through circuitry with an amplitude-versus-frequency response that isn't flat out to infinity. As the signal deviates across the frequency-response curves of whatever circuitry passes it, its angle modulation is, to some degree, converted to AM—a form of crosstalk between the two modulation types, if we wish to look at it that way. (Variations in system phase linearity also cause distortion and FM-to-AM conversion, because the sidebands do not have the proper phase relationship with respect to each other and with respect to the carrier.)

All we need to do to put this effect to practical use is develop a circuit that does this

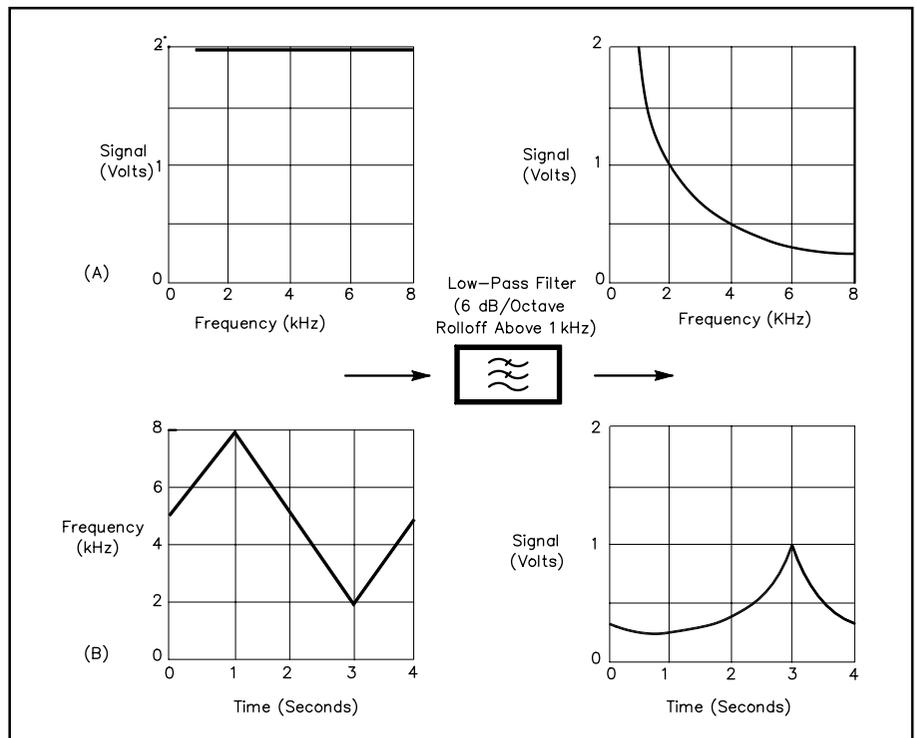


Fig 15.14—Frequency-sweeping a constant-amplitude signal and passing it through a low-pass filter results in an output signal that varies in amplitude with frequency (A). Sweeping the input signal back and forth between two frequency limits causes the output signal's amplitude to vary between two limits (B). This is the principle behind the angle-demodulation process called *frequency discrimination*.

frequency-to-amplitude conversion linearly (and, since more output is better, steeply) across the frequency span of the modulated signal's deviation. Then we envelope-demodulate the resulting AM, and we're in.

Fig 15.15 shows such a circuit—a *discriminator*—and the sort of amplitude-versus-frequency response we expect from it. (It's possible to use an AM receiver to recover understandable audio from a narrow angle-modulated signal by “off-tuning” the signal so its deviation rides up and down on one side of the receiver's IF selectivity curve. This *slope detection* process served as an early, suboptimal form of frequency discrimination.)

Quadrature Detection

It's also possible to demodulate an angle-modulated signal merely by multiplying it with a time-delayed copy of itself in a double-balanced mixer (Fig 15.16). For simplicity's sake, we'll represent the mixer's RF input signal as just a sine wave with an amplitude, A

$$A \sin(2\pi ft) \quad (11)$$

and its time-delayed twin, fed to the mixer's LO input, as a sine wave with an amplitude, A , and a time delay of d :

$$A \sin[2\pi f(t + d)] \quad (12)$$

Setting this special mixing arrangement into motion, we see

$$A \sin(2\pi ft) \times A \sin(2\pi f(t + d)) = \frac{A^2}{2} \cos(2\pi fd) - \frac{A^2}{2} \cos(2\pi fd) \cos(2 \times 2\pi ft) + \frac{A^2}{2} \sin(2\pi fd) \sin(2 \times 2\pi ft) \quad (13)$$

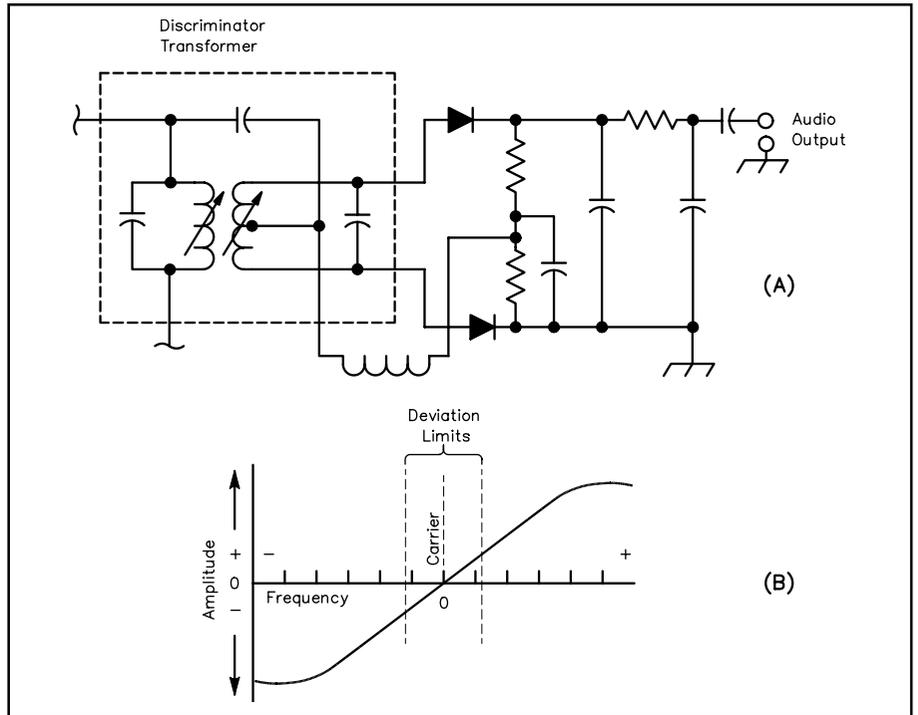


Fig 15.15—A *discriminator* (A) converts an angle-modulated signal's deviation into an amplitude variation (B) and envelope-detects the resulting AM signal. For undistorted demodulation, the discriminator's amplitude-versus-frequency characteristic must be linear across the input signal's deviation. A *crystal discriminator* uses a crystal as part of its frequency-selective circuitry.

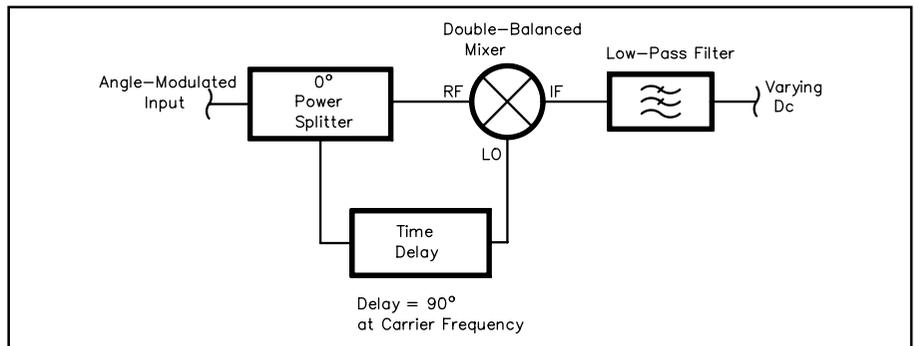


Fig 15.16—In *quadrature detection*, an angle-modulated signal multiplies with a time-delayed copy of itself to produce a dc voltage that varies with the amplitude and polarity of its phase or frequency excursions away from the carrier frequency. A practical quadrature detector can be as simple as a 0° power splitter (that is, a power splitter with in-phase outputs), a diode double-balanced mixer, a length of coaxial cable $1/4\lambda$ (electrical) long at the carrier frequency, and a bit of low-pass filtering to remove the detector output's RF components. IC quadrature detectors achieve their time delay with one or more resistor-loaded tuned circuits (Fig 15.17).

Two of the three outputs—the second and third terms—emerge at twice the input frequency; in practice, we’re not interested in these, and filter them out. The remaining term—the one we’re after—varies in amplitude and sign according to how far and in what direction the carrier shifts away from its resting or center frequency (at which the time delay, d , causes the mixer’s RF and LO inputs to be exactly 90° out of phase—in *quadrature*—with each other). We can examine this effect by replacing f in [equations 11 and 12](#) with the sum term $f_c + f_s$, where f_c is the center frequency and f_s is the frequency shift. A 90° time delay is the same as a quarter cycle of f_c , so we can restate d as

$$d = \frac{1}{4f_c} \quad (14)$$

The first term of the detector’s output then becomes

$$\begin{aligned} & \frac{A^2}{2} \cos(2\pi(f_c + f_s)d) \\ &= \frac{A^2}{2} \cos\left(2\pi(f_c + f_s)\frac{1}{4f_c}\right) \\ &= \frac{A^2}{2} \cos\left(\frac{\pi}{2} + \frac{\pi f_s}{2f_c}\right) \end{aligned} \quad (15)$$

When f_s is zero (that is, when the carrier is at its center frequency), this reduces to

$$\frac{A^2}{2} \cos\left(\frac{\pi}{2}\right) = 0 \quad (16)$$

As the input signal shifts higher in frequency than f_c , the detector puts out a positive dc voltage that increases with the shift. When the input signal shifts lower in frequency than f_c , the detector puts out a negative dc voltage that increases with the shift. The detector therefore recovers the input signal’s frequency or phase modulation as an amplitude-varying dc voltage that shifts in sign as f_s varies around f_c —in other words, as ac. We have demodulated FM by means of quadrature detection.

An ideal quadrature detector puts out 0 V dc when no modulation is present (with the carrier at f_c). The output of a real quadrature detector may include a small *dc offset* that requires compensation. If we need the detector’s response all the way down to dc, we’ve got it; if not, we can put a suitable blocking capacitor in the output line for ac-only coupling.

Quadrature detection is more common than frequency discrimination nowadays because it doesn’t require a special discriminator transformer, and because the necessary balanced-detector circuitry can easily be implemented in IC structures along with limiters and other receiver circuitry. The synchronous [AM detector project](#) later in this chapter uses such a chip, the Philips Components-Signetics NE604A, to do limiting and phase detection as part of a phase-locked loop (PLL); [Fig 15.17](#) shows another example.

PLL Angle Demodulation

Back at [Fig 15.14](#), we saw how a PLL can be used as an angle modulator. A PLL also makes a fine angle demodulator. Applying an angle-modulated signal to a PLL keeps its phase detector and VCO hustling to maintain loop lock through the input signal’s angle variations. The loop’s error voltage therefore tracks the input signal’s modulation, and its variations mirror the modulation signal. Turning the loop’s varying dc error voltage into audio is just a blocking capacitor away.

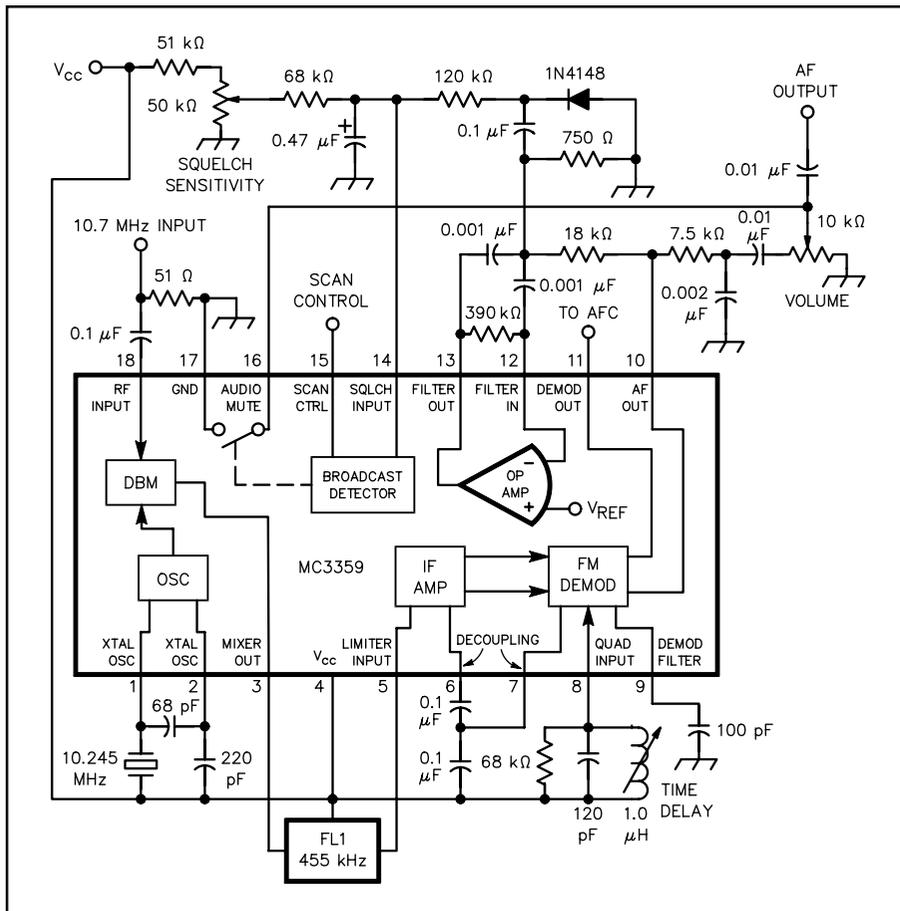


Fig 15.17—The Motorola MC3359 is one of many FM subsystem ICs that include limiter and quadrature-detection circuitry. The TIME DELAY coil is adjusted for minimum recovered-audio distortion.

to suppress and ignore static, manmade electrical noise and (through an angle-modulation-receiver characteristic called *capture effect*) cochannel signals sufficiently weaker than the desired signal. AM-noise immunity is not intrinsic to angle modulation, however; it must be designed into the angle-modulation receiver.

If we note the progress of A from the left to the right side of the equal sign in [equation 13](#), we realize that the amplitude of a quadrature detector's input signal affects the amplitude of a quadrature detector's three output signals. A quadrature detector therefore responds to AM, and so does a frequency discriminator. To achieve FM's storied noise immunity, then, these angle demodulators must be preceded by *limiting* circuitry that removes all amplitude variations from the incoming signal.

PRACTICAL BUILDING BLOCKS FOR MIXING, MODULATION AND DEMODULATION

So far, we've tended to look at mixing as a process that frequency-shifts one sinusoidal wave by mixing it with another. We need to expand our thinking to other cases, however, since it turns out that many practical mixers work best with *square-wave* signals applied to their LO inputs.

Sine-Wave Mixing, Square-Wave Mixing

Thinking of mixers as multiplying sine waves implies that mixers act like tiny analog computers performing millions of multiplications per second. It's certainly possible to build mixers this way by using an IC mixer circuit ([Fig 15.18](#)) conceived in 1967 by Barrie Gilbert and widely known

Although we can't convey a dc level by directly modulating the VCO in a PLL angle modulator, a PLL demodulator can respond down to dc quite nicely. A constant frequency offset from f_c (a dc component) simply causes a PLL demodulator to swing its VCO over to the new input frequency, resulting in a proportional dc offset on the VCO control-voltage line. Another way of looking at the difference between a PLL angle modulator and a PLL angle demodulator is that a PLL demodulator works with a varying reference signal (the input signal), while a PLL angle modulator generally doesn't.

Amplitude Limiting Required

By now, it's almost household knowledge that FM radio communication systems are superior to AM in their ability

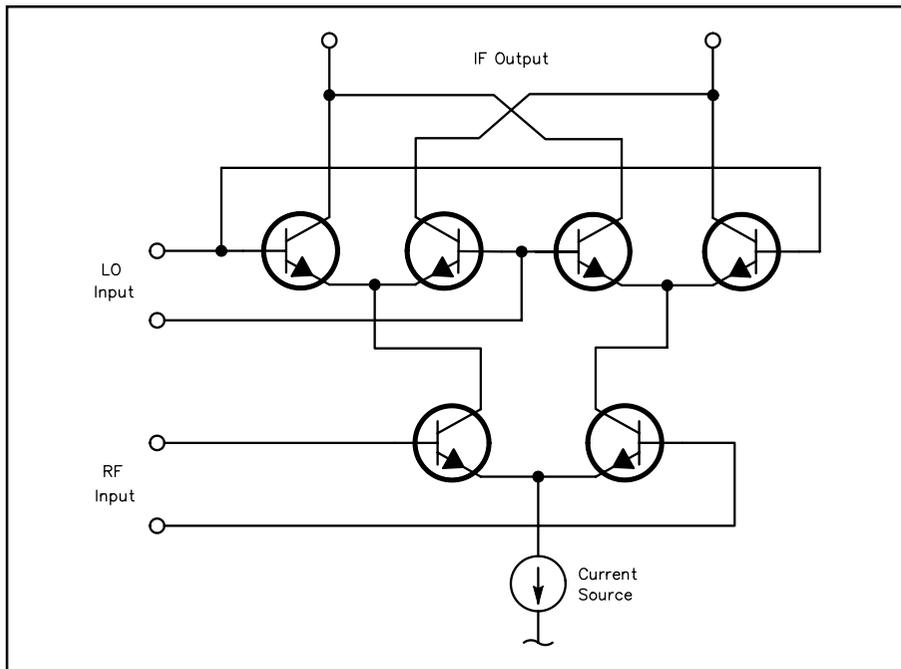


Fig 15.18—The Gilbert cell mixer. The Motorola MC1496 and Philips Components-Signetics NE602A are based on this circuit.

as the Gilbert cell. (Gilbert himself was not responsible for this eponym; indeed, he has noted that a prior art search at the time found that essentially the same idea—used as a “synchronous detector” and not as true mixer—had already been patented by H. E. Jones.) A Gilbert cell consists of two differential transistor pairs whose bias current is controlled by one of the input signals. The other signal drives the differential pairs’ bases, but only after being “predistorted” in a diode circuit. (This circuit distorts the signal equally and oppositely to the inherent distortion of the differential

pair.) The resulting output signal is an accurate multiplication of the input voltages.

Early Gilbert-cell ICs, such as the Motorola MC1495 multiplier, had a number of disadvantages, including critical external adjustments, narrow bandwidth, and limited dynamic range. Modern Gilbert cells, such as the Burr-Brown MPY600 and Analog Devices AD834, overcome most of these disadvantages, and have led to an increase in usage of analog multipliers as mixers. Most practical radio mixers do not work exactly as analog multipliers, however. In practice, they act more like fast analog *switches*.

In using a mixer as a fast switching device, we feed its LO input with a single-frequency square wave rather than a sine wave, and feed sine waves, audio, or other complex signals to the mixer’s RF input. The RF port serves as the mixer’s “linear” input, and therefore must preferably exhibit low intermodulation and harmonic distortion. Feeding a ± 1 -V square wave into the LO input alternately multiplies the linear input by +1 or -1. Multiplying the RF-port signal by +1 just transfers it to the output with no change. Multiplying the RF-port signal by -1 does the same thing, except that the signal inverts (flips 180° in phase). The LO port need not exhibit low intermodulation and harmonic distortion; all it has to do is preserve the switching signal’s fast rise and fall times.

Using square-wave LO drive allows us to simplify the Gilbert multiplier by dispensing with its predistortion circuitry. The Motorola MC1496, still in wide use despite its age, is an example of this. The Philips Components-Signetics NE602A and its relatives, popular with Amateur Radio experimenters, are modern MC1496 descendants. The vast majority of Gilbert-cell mixers in current use are square-wave LO-drive types. In practice, though, we don’t have to square the LO signals we apply to them to make them work well. All we need to do is drive their LO inputs with a sine wave of sufficient amplitude to overdrive the associated transistors’ bases. This clips the LO waveform, effectively resulting in square-wave drive.

Reversing-Switch Mixers

We can multiply a signal by a square wave without using an analog multiplier at all. All we need is a pair of balun transformers and four diodes (**Fig 15.19A**).

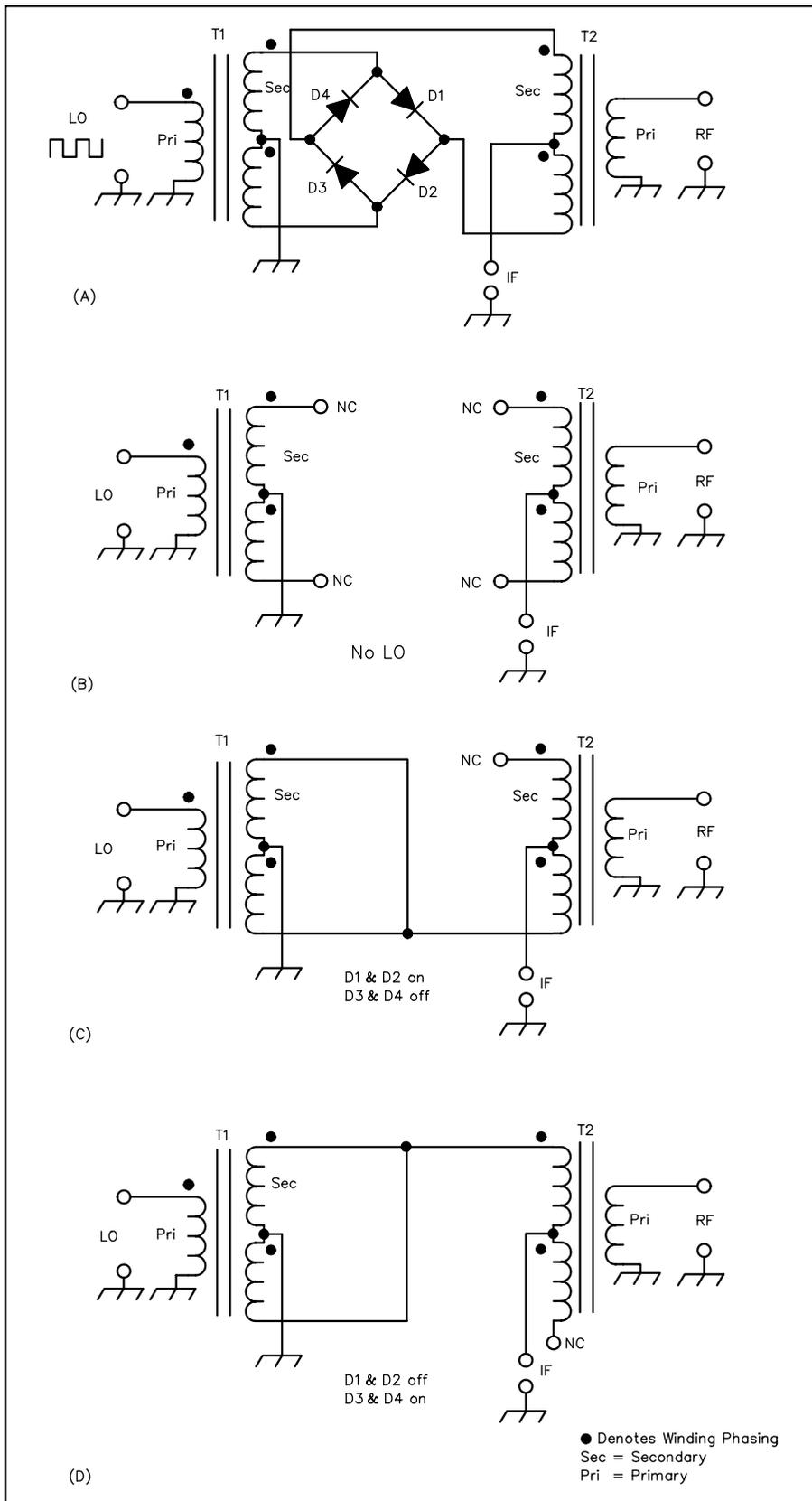


Fig 15.19—A reversing-switch mixer uses fast switching devices, balanced transformers and square-wave LO drive to reverse the RF-port signal’s phase at a rate determined by the LO frequency. (Diodes are shown, but BJTs and FETs can also be used, as we’ll soon see.) With no LO drive (B), no RF-port energy shows up at the IF port because T2’s secondary has no ground return. When the LO waveform goes positive, D1 and D2 turn on and D3 and D4 turn off, unbalancing T2’s secondary and giving the IF port a ground return through T1’s secondary and its center tap. When the LO waveform goes negative, D1 and D2 turn off and D3 and D4 turn on, again unbalancing T2’s secondary and again giving the IF port a ground return. RF-port energy gets through to the IF port in both states; alternately operating T2’s secondary halves results in 180° phase reversal from state to state.

With no LO energy applied to the circuit, none of its diodes conduct (Fig 15.19B). RF-port energy (1) can’t make it to the LO port because there’s no direct connection between the secondaries of T1 and T2, and (2) doesn’t produce IF output because T2’s secondary balance results in energy cancellation at its center tap, and because no complete IF-energy circuit exists through T2’s secondary with both of its ends disconnected from ground.

Applying a square wave to the LO port biases the diodes so that, 50% of the time (Fig 15.19C), D1 and D2 are on and D3 and D4 are reverse-biased off. This unbalances T2’s secondary by leaving its upper wire floating and connecting its lower wire to ground through T1’s secondary and center tap. With T2’s secondary unbalanced, RF-port energy emerges from the IF port.

The other 50% of the time (Fig 15.19D), D3 and D4 are on and D1 and D2 are reverse-biased off. This unbalances T2's secondary by leaving its lower wire floating, and connects its upper wire to ground through T1's secondary and center tap. With T2's secondary unbalanced, RF-port energy again emerges from the IF port—shifted 180° relative to the first case because T2's active secondary wires are now, in effect, transposed relative to its primary.

A reversing switch mixer's output spectrum is the same as the output spectrum of a multiplier fed with a square wave. This can be analyzed by thinking of the square wave in terms of its Fourier series equivalent, which consists of the sum of sine waves at the square wave frequency and all of its odd harmonics. The amplitude of the equivalent series' fundamental sine wave is $4/\pi$ times (2.1 dB greater than) the amplitude of the square wave. The amplitude of each harmonic is inversely proportional to its harmonic number, so the third harmonic is only $1/3$ as strong as the fundamental (9.5 dB below the fundamental), the 5th harmonic is only $1/5$ as strong (14 dB below the fundamental) and so on. The input signal mixes with each harmonic separately from the others, as if each harmonic were driving its own separate mixer, just as we illustrated with two sine waves in Fig 15.4. Normally, the harmonic outputs are so widely removed from the desired output frequency that they are easily filtered out, so a reversing-switch mixer is just as good as a sine-wave-driven analog multiplier for most practical purposes, and usually better—for radio purposes—in terms of dynamic range and noise.

An additional difference between multiplier and switching mixers is that a switching mixer's signal flow is reversible. It really only has one dedicated input (the LO input). The other terminals can be thought of as I/O (input/output) ports, since either one can be the input as long as the other is the output.

Conversion Loss

At the instant of peak output voltage, a switching mixer's waveform is a full-wave-rectified sine wave. Thus its average output voltage equals that of a full-wave-rectified sine wave, which we know from power supply theory to be $2/\pi$, or 0.636, times the peak of the sine wave. The conversion loss of an ideal reversing-switch mixer is therefore $20 \log 2/\pi$, or 3.9 dB. This is 2.1 dB better than an ideal multiplier, in which the peak output voltages of the sum and difference products are each half that of (6 dB lower than) the input signal, as reflected in the $1/2$ coefficients in the right-hand terms of equation 7.

The switches and transformers in an ideal reversing-switch mixer dissipate no power, so an ideal reversing-switch mixer is lossless. If the LO driving signal contained no harmonics, half of the mixer's output power would show up in the sum product and half in the difference product, resulting in a 3-dB conversion loss relative to the RF-port signal. Some of the mixer's output goes into the sum and difference products of harmonic mixing, however; hence, the additional 0.9 dB of conversion loss, or 3.9 dB overall for an ideal reversing switch mixer. In practical mixers, switch and transformer losses increase conversion loss to a typical value of 6 dB.

The Diode Double-Balanced Mixer: A Basic Building Block

The most common implementation of a reversing switch mixer is the diode *double-balanced mixer* (DBM). DBMs can serve as mixers (including image-reject types), modulators (including single- and double-sideband, phase, biphasic, and quadrature-phase types) and demodulators, limiters, attenuators, switches, phase detectors, and frequency doublers. In some of these applications, they work in conjunction with power dividers, combiners and hybrids.

The Basic DBM Circuit

We have already seen the basic diode DBM circuit (Fig 15.19). In its simplest form, a DBM contains two or more unbalanced-to-balanced transformers and a Schottky diode ring consisting of $4 \times n$ diodes, where n is the number of diodes in each leg of the ring. Each leg commonly consists of up to four diodes.

As we've seen, the degree to which a mixer is *balanced* depends on whether either, neither or both

of its input signals (RF and LO) emerge from the IF port along with mixing products. An unbalanced mixer suppresses neither its RF nor its LO; both are present at its IF port. A single-balanced mixer suppresses its RF or LO, but not both. A double-balanced mixer suppresses its RF *and* LO inputs. Diode and transformer uniformity in the Fig 15.19 circuit results in equal LO potentials at the center taps of T1 and T2. The LO potential at T1's secondary center tap is zero (ground); therefore, the LO potential at the IF port is zero.

Balance in T2's secondary likewise results in an RF null at the IF port. The RF potential between the IF port and ground is therefore zero—except when the DBM's switching diodes operate, of course!

The Fig 15.19 circuit normally also affords high RF-IF isolation because its balanced diode switching precludes direct connections between T1 and T2. A diode DBM can be used as a current-controlled switch or attenuator by applying dc to its IF port, albeit with some distortion. This causes opposing diodes (D2 and D4, for instance) to conduct to a degree that depends on the current magnitude, connecting T1 to T2.

One extension of the single-diode-ring DBM is a *double* double-balanced mixer (DDBM) with high dynamic range and larger signal handling capability than a single-ring design. Fig 15.20 diagrams such a DDBM, which uses transmission-line transformers and two diode rings. This type of mixer has higher 1-dB compression point (usually 3 to 4 dB lower than the LO drive) than a DBM. Low distortion is a typical characteristic of DDBMs. Depending on the ferrite core material used (ferrites with a magnetic permeability— μ —of 100 to 15,000), frequencies as low as a few hundred hertz and as high as a few gigahertz can be covered.

Diode DBM Components

Commercially manufactured diode DBMs generally consist of: (A) a supporting base; (B) a diode ring; (C) two or more ferrite-core transformers commonly wound with two or three twisted-pair wires; (D) encapsulating material; and (E) an enclosure.

Diodes

Hot-carrier (Schottky) diodes are the devices of choice for diode-DBM rings because of their low ON resistance, although ham-built DBMs for noncritical MF/HF use commonly use switching diodes like the 1N914 or 1N4148. The forward voltage drop, V_f , across each diode in the ring determines the mixer's optimum local-oscillator drive level. Depending on the forward voltage drop of each of its diodes and the number of diodes in each ring leg, a diode DBM may be categorized as a Level 0, 3, 7, 10, 13, 17, 23 or 27 device. The numbers indicate the mixer's optimal LO

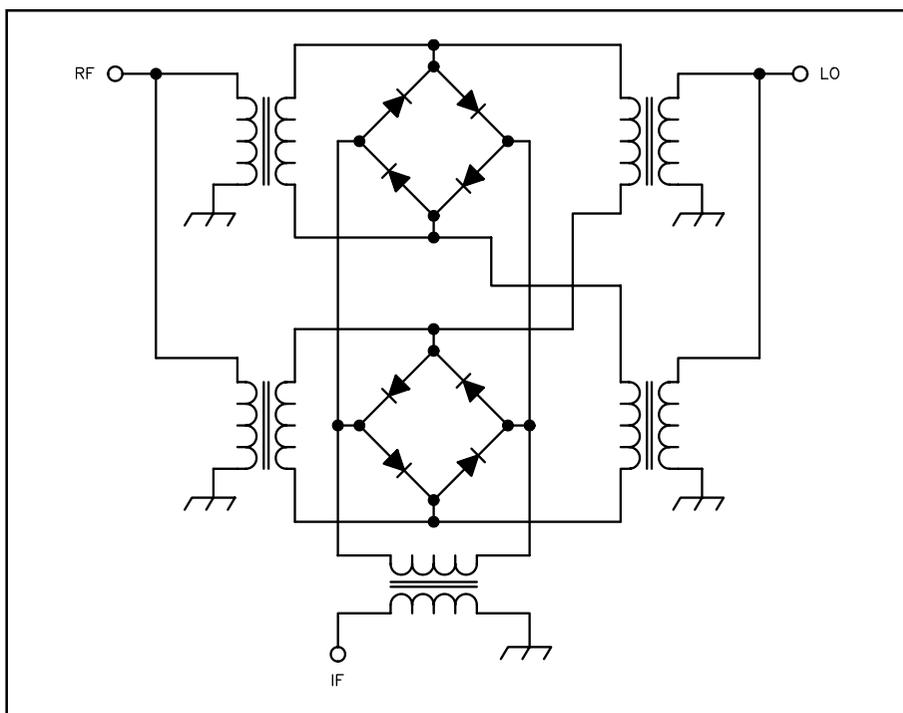


Fig 15.20—Five transmission-line transformers and two Schottky-quad rings form this double double-balanced mixer (DDBM). Such designs can provide lower distortion, better signal-handling capability and higher interport isolation than single-ring designs.

drive level in dBm. As a rule of thumb, the LO signal must be must 20 dB larger than the RF and IF signals for proper operation. This ensures that the LO signal, rather than the RF or IF signals, switches the mixer's diodes on and off—a critical factor in minimizing IMD and maximizing dynamic range.

Schottky diodes are characterized by loss and contact resistance (R_s), junction capacitance (C_j), and forward voltage drop (V_f) at a known current, typically 1 mA or 10 mA. The lower the diode-to-diode V_f difference in millivolts, the better the diode match at dc. (Some early diode DBM designs used diodes in series with a parallel resistor/capacitor combination for automatic biasing.) Better diode matching (in V_f and C_j) results in higher isolation among the ports. Diodes capable of operating at higher frequencies have lower junction capacitance and lower parasitic inductance. **Fig 15.21** shows the equivalent circuit for Schottky diodes of three package types.

Manufacturers of diodes suitable for DBMs characterize their diodes as low-barrier, medium-barrier, high-barrier and very-high-barrier (usually two or more diodes in each leg), with typical V_f values of 220 mV, 350 mV, 600 mV and 1 V or more, respectively. **Fig 15.22** shows a typical current-voltage (I-V) characteristic for a low-barrier Schottky quad capable of operating up to 4 GHz. Note that as current through the diodes increases, the V_f difference among the ring's diodes also increases, affecting the balance.

At higher frequencies, diode packaging becomes critical and expensive. As the frequency of operation increases, the effect of junction capacitance and package capacitance cannot be ignored. Part or all of the capacitance can be compensated at the mixer's highest operating frequency by properly designing the unbalanced-to-balanced transformers. The transformer inductance and diode junction capacitance form a low-pass network with its cut-off frequency higher than the frequency of operation. Compensated in this way, diodes with a junction capacitance of 0.2 pF can be used up to 8 to 10 GHz.

Transformers

From the DBM schematic shown in **Fig 15.19**, it's clear that the LO and RF transformers are unbalanced on the input side and balanced on the diode side. The diode ends of the balanced ports are 180° out of phase throughout the frequency range of interest. This property causes signal cancellations that result in higher port-to-port isolation. **Fig 15.23** plots LO-RF and LO-IF isolation versus frequency for Synergy Microwave's CLP-4A3 DBM. Isolations on the order of 70 dB occur at lower end of the band as a direct result of the balance among the four diode-ring legs and the RF phasing of the balanced ports.

As we learned in our discussion of generic switching mixers, transformer efficiency plays an important role in determining a mixer's conversion loss and drive-level requirement. Core loss, copper loss and impedance mismatch all contribute to transformer losses.

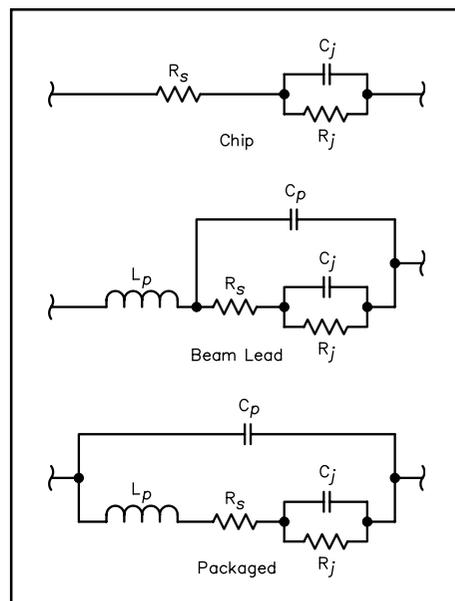


Fig 15.21—Its semiconductive properties aside, a Schottky diode can be represented as a network consisting of resistance, capacitance and/or inductance. Of these, the junction capacitance (C_j) plays an especially critical role in a double-balanced mixer's high-end response. (R_j =junction resistance, R_s =contact resistance, L_p =parasitic inductance and C_p =parasitic capacitance.)

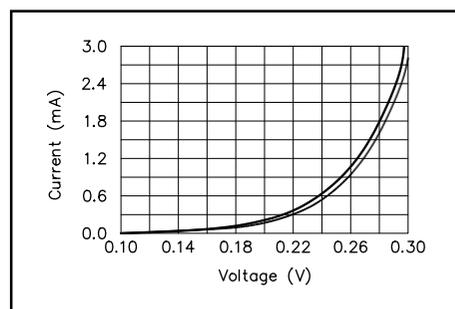


Fig 15.22—Current-voltage (I-V) characteristic for Schottky diode quad, showing worst-case voltage imbalance (the spread between the two curves) among the four diodes.

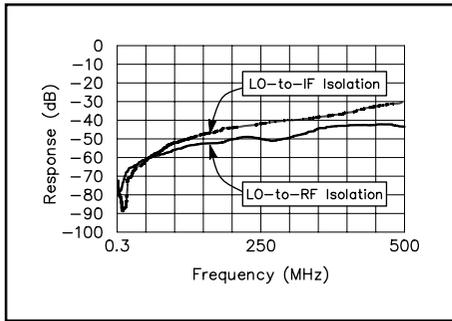


Fig 15.23—A diode DBM’s port-to-port isolation depends on how well its diodes match and how well its transformers are balanced. This graph shows LO-IF and LO-RF isolation versus frequency for a Synergy Microwave CLP-4A3 mixer.

of winding turns, determine the transformer’s lower frequency limit. Covering a specific frequency range requires a compromise in the number of turns used with a given core. Increasing a transformer’s core size and number of turns improves its low-frequency response. Cores may be stacked to meet low-frequency performance specs.

Inexpensive mixers operating up to 2 GHz most commonly use twisted trifilar (three-wire) windings made of a wire size between #36 and #32. The number of twists per unit length of wire determines a winding’s characteristic impedance. Twisted wires are analogous to transmission lines, and can be analyzed in terms of distributed interwinding capacitance. Decreasing the number of twists lowers the interwinding capacitance and increases the frequency of operation. On the other hand, using fewer twists per inch than four makes winding difficult because the wires tend to separate instead of behaving as a single cable.

The transmission-line effect predominates at the higher end of a transformer’s frequency range. If two impedances, Z_1 and Z_2 , need to be matched through a transmission line of characteristic impedance, Z_0 , then

$$Z_0 = \sqrt{Z_1 \times Z_2} \quad (17)$$

Fig 15.24 shows two types of transformers using twisted wires: (A) a three-wire type in which the primary winding is isolated from the secondary winding with a center tap, and (B) a two-wire (transmission-line) type in

which two sets of transmission lines are interconnected to form a center tap at the secondary with a direct connection between primary and secondary. The primary-to-secondary turns ratio determines the impedance match as shown in equation 17. The properties of these two transformer types can be summarized as follows:

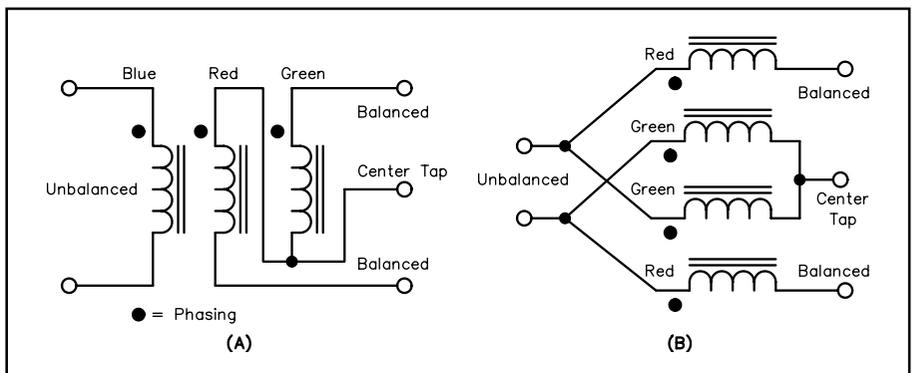


Fig 15.24—Transformers for DBMs: three-wire (A), and transmission-line (B).

1. By virtue of its construction, the three-wire transformer is less symmetrical at higher frequencies than the transmission-line type.

2. The transformers' lower cutoff frequency (f_L) is determined by the equation

$$\omega L > 4R \quad (18)$$

where

L = inductance of the winding

R = system impedance; for example, 50Ω , 75Ω and so on; and

$\omega = 2\pi f_L$.

3. The transmission-line transformer's upper cutoff frequency (f_H) is determined by the highest frequency at which its wires' twists (that is, the coupling between them) allow it to function as a transmission line of the proper characteristic impedance.

4. Transformers convert one impedance, Z_1 (primary) to another, Z_2 (secondary) according to the relationship

$$Z_2 = Z_1 (N)^2 \quad (19)$$

where N = secondary to primary turns ratio. Within certain limits, if Z_1 is varied, Z_2 also varies to a new value multiplied by N^2 . Thus, a mixer designed for a $50\text{-}\Omega$ system may work in a $75\text{-}\Omega$ system with minor modifications.

Diode DBMs in Practice

Fig 15.25 shows the wiring of a typical commercial DBM made with toroidal cores. The wires are wrapped around the package pins and diode leads, and then soldered. In this unit, the LO transformer's primary winding connects across pins 7 and 8; the RF-transformer primary, across pins 1 and 2. The pin pairs 3-4 and 5-6 are connected externally to form the transformers' secondary center taps, one of which (5-6, that of the LO transformer) connects to a common ground point while the other (3-4, that of the RF transformer) serves as the IF port.

The DBM shown in Fig 15.25 has a dc-coupled IF port. If necessary, this DBM can be operated at a particular polarity (positive or negative) by appropriately connecting the LO, RF, IF and common ground points.

DBM Specifications

Most of the parameters important in building or selecting a diode DBM also apply to other mixer types. They include: conversion loss and amplitude flatness across the required IF bandwidth; variation of conversion loss with input frequency; variation of conversion loss with LO drive, 1-dB compression point; LO-RF, LO-IF and RF-IF isolation; intermodulation products; noise figure (usually within 1 dB of conversion loss); port SWR; and dc offset, which is directly related to isolation among the RF, LO and IF ports.

Conversion Loss

Fig 15.26 shows a plot of conversion loss versus intermediate frequency in a typical DBM. The curves show conversion loss for two fixed RF-port signals, one at 100 kHz and the another at 500 MHz, while varying the LO frequency from 100 kHz to 500 MHz.

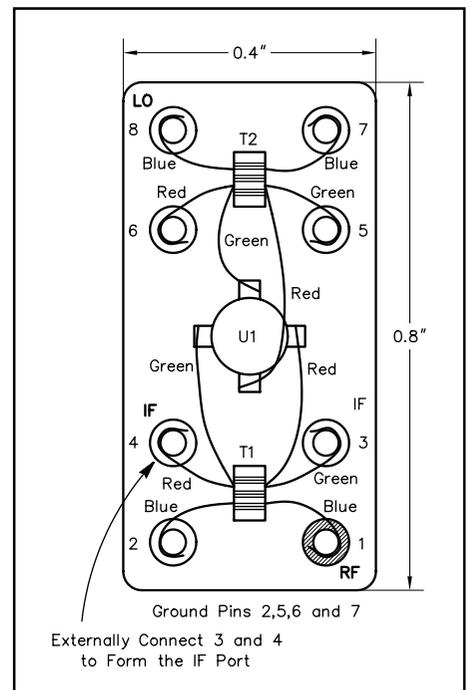


Fig 15.25—How a typical commercial DBM is wired. The use of different wire colors for the transformers' various windings speeds assembly and minimizes error. U1, a Schottky-diode quad, contains D1-D4 of Fig 15.19.

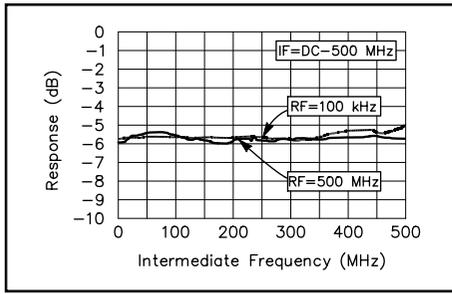


Fig 15.26—Conversion loss versus frequency for a typical diode DBM. The LO drive level is +7 dBm.

Fig 15.27 plots a diode DBM’s simulated output spectrum. Note that the RF input is -20 dBm and the IF output (the frequency difference between the RF and LO signals) is -25 dBm, implying a conversion loss of 5 dB. This figure also applies to the sum of both signals (RF + LO).

We minimize a diode DBM’s conversion loss, noise figure and intermodulation by keeping its LO drive high enough to switch its diodes on fully and rapidly. **Fig 15.28** plots noise figure for the DDBM shown in **Fig 15.20**. Its 4-dB noise figure assumes ideal transformers and somewhat idealized diodes; typical mixers have a noise figure of 5 to 6 dB. **Fig 15.29** plots conversion gain (loss) for the same mixer circuit.

Insufficient LO drive results in increased noise figure and conversion loss. IMD also increases because RF-port signals have a greater chance to control the mixer diodes when the LO level is too low.

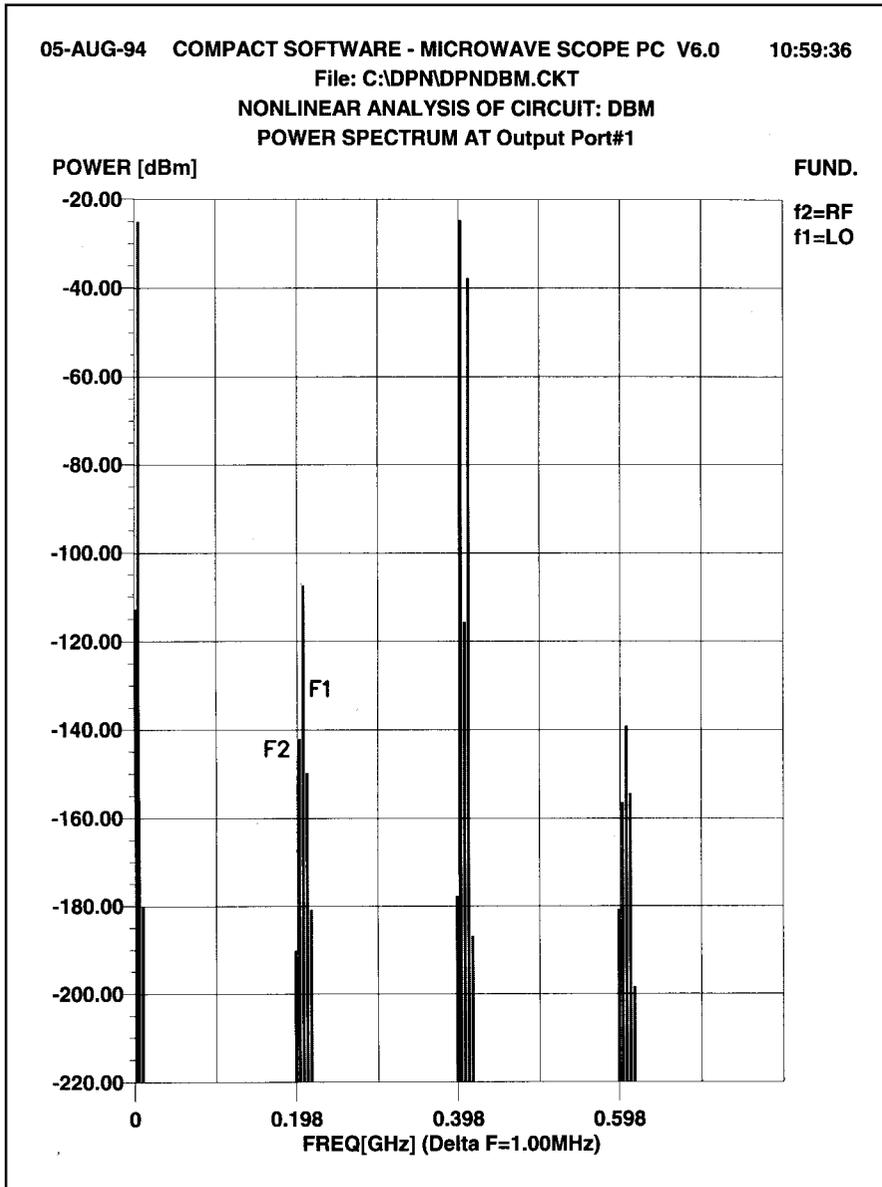


Fig 15.27—Simulated diode-DBM output spectrum. Note that the desired output products (the highest two products, RF – LO and RF + LO) emerge at a level 5 dB below the mixer’s RF input (-20 dBm). This indicates a mixer conversion loss of 5 dB. (*Microwave SCOPE* simulation)

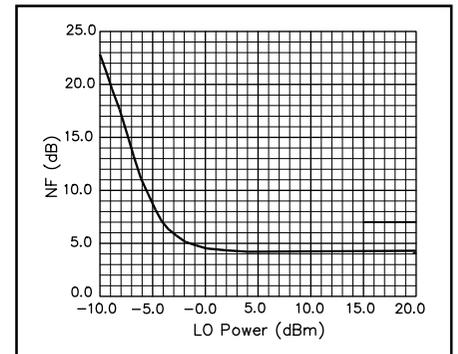


Fig 15.28—Noise figure versus LO drive for a DDBM built along the lines of **Fig 15.20.**

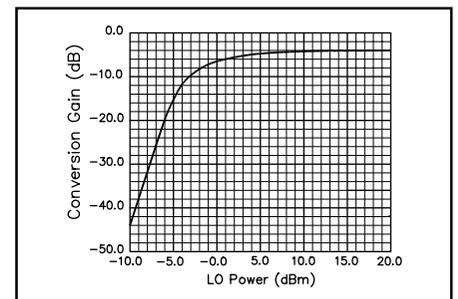


Fig 15.29—Conversion gain (loss) for a DDBM. Increasing a mixer’s LO level beyond that sufficient to turn its switching devices all the way on merely makes them dissipate more LO power and does not improve performance.

Testing and Calculating Intermodulation Distortion in Receivers

Second and third-order IMD can be measured using the setup of **Fig A**. The outputs of two signal generators are combined in a 3-dB hybrid coupler. Such couplers are available from various companies, and can be homemade. The 3-dB coupler should have low loss and should itself produce negligible IMD. The signal generators are adjusted to provide a known signal level at the output of the 3-dB coupler, say, -20 dBm for each of the two signals. This combined signal is then fed through a calibrated variable attenuator to the device under test. The shielding of the cables used in this system is important: At least 90 dB of isolation should exist between the high-level signal at the input of the attenuator and the low-level signal delivered to the receiver.

The measurement procedure is simple: adjust the variable attenuator to produce a signal of known level at the frequency of the expected IMD product ($f_1 \pm f_2$ for second-order, $2f_1 - f_2$ or $2f_2 - f_1$ for third-order IMD).

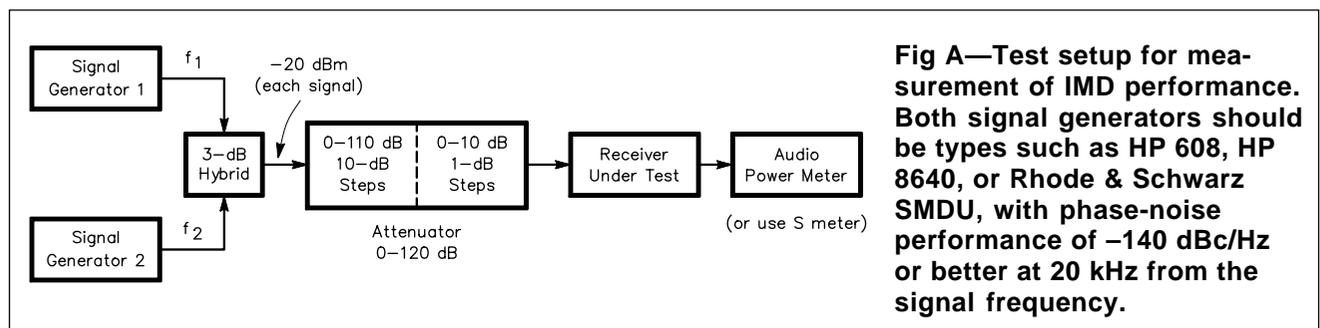
To do this, of course, you have to figure out what equivalent input signal level at the receiver's operating frequency corresponds to the level of the IMD product you are seeing. There are several ways of doing this. One way—the way used by the ARRL Lab in their receiver tests—uses the minimum discernible signal. This is defined as the signal level that produces a 3-dB increase in the receiver audio output power. That is, you measure the receiver output level with no input signal, then insert a signal at the operating frequency and adjust the level of this input signal until the output power is 3 dB greater than the no-signal power. Then, when doing the IMD measurement, you adjust the attenuator of Fig A to cause a 3-dB increase in receiver output. The level of the IMD product is then the same as the MDS level you measured.

There are several things I dislike about doing the measurement this way. The problem is that you have to measure noise power. This can be difficult. First, you need an RMS voltmeter or audio power meter to do it at all. Second, the measurement varies with time (it's noise!), making it difficult to nail down a number. And third, there is the question of the audio response of the receiver; its noise output may not be flat across the output spectrum. So I prefer to measure, instead of MDS, a higher reference level. I use the receiver's S meter as a reference. I first determine the input signal level it takes to get an S1 reading. Then, in the IMD measurement, I adjust the attenuator to again give an S1 reading. The level of the IMD product signal is now equal to the level I measured at S1. Note that this technique gives a different IMD level value than the MDS technique. That's OK, though. What we are trying to determine is the *difference* between the level of the signals applied to the receiver input and the level of the IMD product. Our calculations will give the same result whether we measure the IMD product at the MDS level, the S1 level or some other level.

An easy way to make the reference measurement is with the setup of Fig A. You'll have to switch in a lot of attenuation (make sure you have an attenuator with enough range), but doing it this way keeps all of the possible variations in the measurement fairly constant. And this way, the difference between the reference level and the input level needed to produce the desired IMD product signal level is simply the difference in attenuator settings between the reference and IMD measurements.

Calculating Intercept Points

Once we know the levels of the signals applied to the receiver input and the level of the IMD product, we can easily calculate the intercept point using the following equation:



$$IP_n = \frac{n \times P_A - P_{IM_n}}{n-1} \quad (A)$$

Here, n is the order, P_A is the receiver input power (of one of the input signals), P_{IM_n} is the power of the IMD product signal, and IP_n is the n th-order intercept point. All powers should be in dBm. For second and third-order IMD, equation A results in the equations:

$$IP_2 = \frac{2 \times P_A - P_{IM_2}}{2-1} \quad (B)$$

$$IP_3 = \frac{3 \times P_A - P_{IM_3}}{3-1} \quad (C)$$

You can measure higher-order intercept points, too.

Example Measurements

To get a feel for this process, it's useful to consider some actual measured values.

The first example is a Rohde & Schwarz model EK085 receiver with digital preselection. For measuring second-order IMD, signals at 6.00 and 8.01 MHz, at -20 dBm each, were applied at the input of the attenuator. The difference in attenuator settings between the reference measurement and the level needed to produce the desired IMD product signal level was found to be 125 dB. The calculation of the second-order IP is then:

$$\begin{aligned} IP_2 &= \frac{2(-20 \text{ dBm}) - (-20 \text{ dBm} - 125 \text{ dB})}{2-1} \\ &= -40 \text{ dB} + 20 \text{ dBm} + 125 \text{ dB} \\ &= +105 \text{ dBm} \end{aligned}$$

For IP_3 , we set the signal generators for 0 dBm at the attenuator input, using frequencies of 14.00 and 14.01 MHz. The difference in attenuator settings between the reference and IMD measurements was 80 dB, so:

$$\begin{aligned} IP_3 &= \frac{3(0 \text{ dBm}) - (0 \text{ dBm} - 80 \text{ dB})}{3-1} \\ &= \frac{0 \text{ dBm} + 80 \text{ dB}}{2} = +40 \text{ dBm} \end{aligned}$$

We also measured the IP_3 of a Yaesu FT-1000D at the same frequencies, using attenuator-input levels of -10 dBm. A difference in attenuator readings of 80 dB resulted in the calculation:

$$\begin{aligned} IP_3 &= \frac{3(-10 \text{ dBm}) - (-10 \text{ dBm} - 80 \text{ dB})}{3-1} \\ &= \frac{-30 \text{ dBm} + 10 \text{ dBm} + 80 \text{ dB}}{2} = \frac{-20 \text{ dBm} + 80 \text{ dB}}{2} \\ &= +30 \text{ dBm} \end{aligned}$$

Synthesizer Requirements

To be able to make use of high third-order intercept points at these close-in spacings requires a low-noise LO synthesizer. You can estimate the required noise performance of the synthesizer for a given IP_3 value. First, calculate the value of receiver input power that would cause the IMD product to just come out of the noise floor, by solving equation A for P_A , then take the difference between the calculated value of P_A and the noise floor to find the dynamic range. Doing so gives the equation:

$$ID_3 = \frac{2}{3} (IP_3 + P_{\min}) \quad (D)$$

Where ID_3 is the third-order IMD dynamic range in dB and P_{\min} is the noise floor in dBm. Knowing the receiver bandwidth, BW (2400 Hz in this case) and noise figure, NF (8 dB) allows us to calculate the noise floor, P_{\min} :

$$\begin{aligned}P_{\min} &= -174 \text{ dBm} + 10 \log(\text{BW}) + \text{NF} \\ &= -174 \text{ dBm} + 10 \log(2400) + 8 \\ &= -132 \text{ dBm}\end{aligned}$$

The synthesizer noise should not exceed the noise floor when an input signal is present that just causes an IMD product signal at the noise floor level. This will be accomplished if the synthesizer noise is less than:

$$\begin{aligned}ID + 10 \log(\text{BW}) &= 114.7 \text{ dB} + 10 \log(2400) \\ &= 148.5 \text{ dBc/Hz}\end{aligned}$$

in the passband of the receiver. Such synthesizers hardly exist.—Dr. Ulrich L. Rohde, KA2WEU

Dynamic Range: Compression, Intermodulation and More

The output of a linear stage—including a mixer, which we want to act as a linear frequency shifter—tracks its input signal decibel by decibel, every 1-dB change in its input signal(s) corresponding to an identical 1-dB output change. This is the stage's *first-order* response.

Because no device is perfectly linear, however, two or more signals applied to it generate sum and difference frequencies. These IMD products occur at frequencies and amplitudes that depend on the order of the IMD response as follows:

- *Second-order* IMD products change 2 dB for every decibel of input-signal change (this figure assumes that the IMD comes from equal-level input signals), and appear at frequencies that result from the simple addition and subtraction of input-signal frequencies. For example, assuming that its input bandwidth is sufficient to pass them, an amplifier subjected to signals at 6 and 8 MHz will produce second-order IMD products at 2 MHz ($8-6$) and 14 MHz ($8+6$).
- *Third-order* IMD products change 3 dB for every decibel of input-signal change (this also assumes equal-level input signals), and appear at frequencies corresponding to the sums and differences of twice one signal's frequency plus or minus the frequency of another. Assuming that its input bandwidth is sufficient to pass them, an amplifier subjected to signals at 14.02 MHz (f_1) and 14.04 MHz (f_2) produces third-order IMD products at 14.00 ($2f_1 - f_2$), 14.06 ($2f_2 - f_1$), 42.08 ($2f_1 + f_2$) and 42.10 ($2f_2 + f_1$) MHz. The subtractive products (the 14.02 and 14.04-MHz products in this example) are close to the desired signal and can cause significant interference. Thus, third-order-IMD performance is of great importance in receiver mixers and RF amplifiers.

It can be seen that the IMD order determines how rapidly IMD products change level per unit change of input level. N th-order IMD products therefore change by n dB for every decibel of input-level change.

IMD products at orders higher than three can and do occur in communication systems, but the second- and third-order products are most important in receiver front ends. In transmitters, third- and higher-odd-order products are important because they widen the transmitted signal.

Intercept Point

The second type of dynamic range concerns the receiver's *intercept point*, sometimes simply referred to as *intercept*. Intercept point is typically measured by applying two or three signals to the antenna input,

tuning the receiver to count the number of resulting spurious responses, and measuring their level relative to the input signal.

Because a device's IMD products increase more rapidly than its desired output as the input level rises, it might seem that steadily increasing the level of multiple signals applied to an amplifier would eventually result in equal desired-signal and IMD levels at the amplifier output. Real devices are incapable of doing this, however. At some point, every device *overloads*, and changes in its output level no longer equally track changes at its input. The device is then said to be operating in *compression*; the point at which its first-order response deviates from linearity by 1 dB is its *1-dB compression point*. Pushing the process to its limit ultimately leads to *saturation*, at which point input-signal increases no longer increase the output level.

The power level at which a device's second-order IMD products equal its first-order output (a point that must be extrapolated because the device is in compression by this point) is its *second-order intercept point*. Likewise, its *third-order intercept point* is the power level at which third-order responses equal the desired signal. **Fig 15.30** graphs these relationships.

Input filtering can improve second-order intercept point; device nonlinearities determine the third, fifth and higher-odd-number intercept points. In preamplifiers, third-order intercept point is directly related to dc input power; in mixers, to the local-oscillator power applied.

Applying Diode DBMs

At first glance, applying a diode DBM is easy: We feed the signal(s) we want to frequency-shift (at or below the maximum level called for in the mixer's specifications, such as -10 dBm for the Mini-Circuits SBL-1 and Synergy Microwave S-1, popular Level 7 parts) to the DBM's RF port, feed the frequency-shifting signal (at the proper level) to the LO port, and extract the sum and difference products from the mixer's IF port.

There's more to it than that, however, because diode DBMs (along with most other modern mixer types) are *termination-sensitive*. That is, their ports—particularly their IF (output) ports—must be resistively terminated with the proper impedance (commonly 50 Ω , resistive). A wideband, resistive output termination is particularly critical if a mixer is to achieve its maximum dynamic range in receiving applications. Such a load can be achieved by

- terminating the mixer in a 50- Ω resistor or attenuator pad (a technique usually avoided in receiving applications because it directly degrades system noise figure);
- terminating the mixer with a low-noise, high-dynamic-range *post-mixer amplifier* designed to exhibit a wideband resistive input impedance; or
- terminating the mixer in a *diplexer*, a frequency-sensitive signal splitter that appears as a two-terminal resistive load at its input while resistively dissipating unwanted outputs and passing desired outputs through to subsequent circuitry.

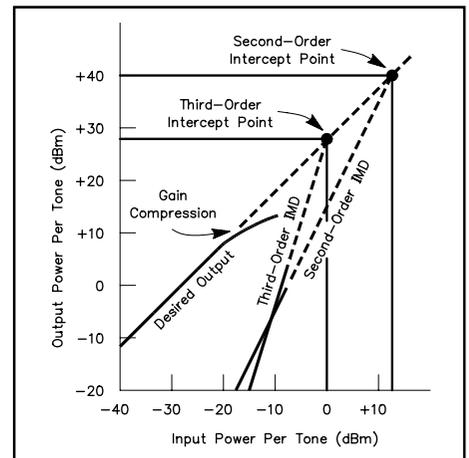


Fig 15.30—A linear stage's output tracks its input decibel by decibel on a 1:1 slope—its *first-order response*. *Second-order intermodulation distortion (IMD) products* produced by two equal-level input signals ("tones") rise on a 2:1 slope—2 dB for every 1 dB of input increase. *Third-order IMD products* likewise increase 3 dB for every 1 dB of increase in two equal tones. For each IMD order n , there is a corresponding *intercept point* IP_n at which the stage's first-order and n th order products are equal in amplitude. The first-order output of real amplifiers and mixers falls off (the device *overloads* and goes into *compression*) before IMD products can intercept it, but intercept point is nonetheless a useful, valid concept for comparing radio system performance. The higher an amplifier or mixer's intercept point, the stronger the input signals it can handle without overloading. The input and output powers shown are for purposes of example; every device exhibits its own particular IMD profile. (After W. Hayward, *Introduction to Radio Frequency Design*, Fig 6.17)

Termination-insensitive mixers are available, but this label can be misleading. Some termination-insensitive mixers are nothing more than a termination-sensitive mixer packaged with an integral post-mixer amplifier. True termination-insensitive mixers are less common and considerably more elaborate. Amateur builders will more likely use one of the many excellent termination-sensitive mixers available in connection with a diplexer, post-mixer amplifier or both.

Fig 15.31 shows one diplexer implementation. In this approach, L1 and C1 form a series-tuned circuit, resonant at the desired IF, that presents low impedance between the diplexer's input and output terminals at the IF. The high-impedance parallel-tuned circuit formed by L2 and C2 also resonates the desired IF, keeping desired energy out of the diplexer's 50-Ω load resistor, R1.

The preceding example is called a *band-pass diplexer*. **Fig 15.32** shows another type: a *high-pass/low-pass diplexer* in which each inductor and capacitor has a reactance of 70.7 Ω at the 3-dB cutoff frequency. It can be used after a “difference” mixer (a mixer in which the IF is the difference between the signal frequency and LO) if the desired IF and its image frequency are far enough apart so that the image power is “dumped” into the network's 51-Ω resistor. (For a “summing” mixer—a mixer in which the IF is the sum of the desired signal and LO—interchange the 50-Ω idler load resistor and the diplexer's “50-Ω Amplifier” connection.) Richard Weinreich, K0UVU, and R. W. Carroll described this circuit in November 1968 *QST* as one of several absorptive TVI filters.

Fig 15.33 shows a BJT post-mixer amplifier design made popular by Wes Hayward, W7ZOI, and John Lawson, K5IRK. RF feedback (via the 1-kΩ resistor) and emitter degeneration (the accoupled 5.6-Ω emitter resistor) work together to keep the stage's input impedance low and uniformly resistive across a wide bandwidth.

Amplitude Modulation with a DBM

We can generate DSB, suppressed-carrier AM with a DBM by feeding the carrier to its RF port and the modulating signal to the IF port. This is a classical *balanced modulator*, and the result—sidebands at radio frequencies corresponding to the carrier signal plus audio and the RF signal minus audio—emerges from the DBM's LO port. If we also want to transmit some carrier along with the sidebands, we can dc-bias the IF port (with a current of 10 to 20 mA) to upset the mixer's balance and keep its diodes from turning all the way off. (This technique is sometimes used for generating CW with a balanced modulator otherwise intended to generate DSB as part of an

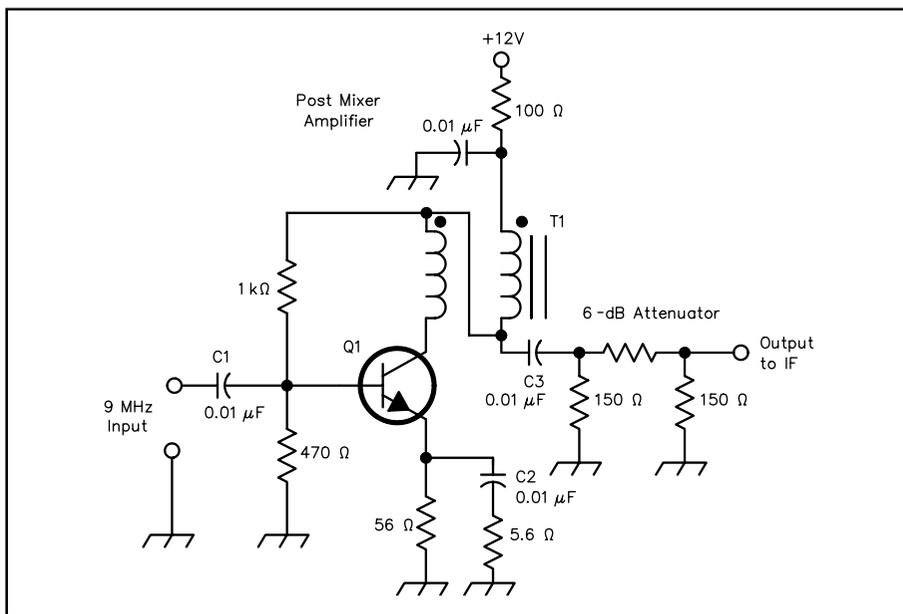
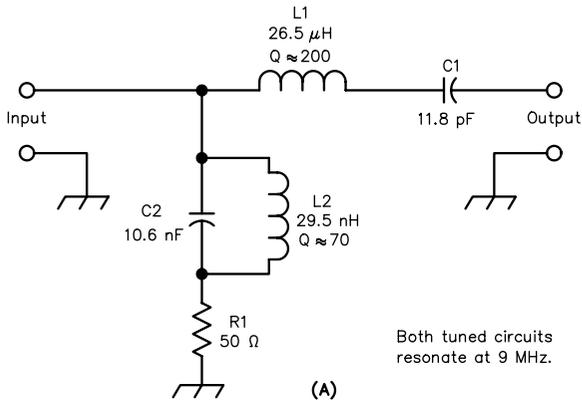
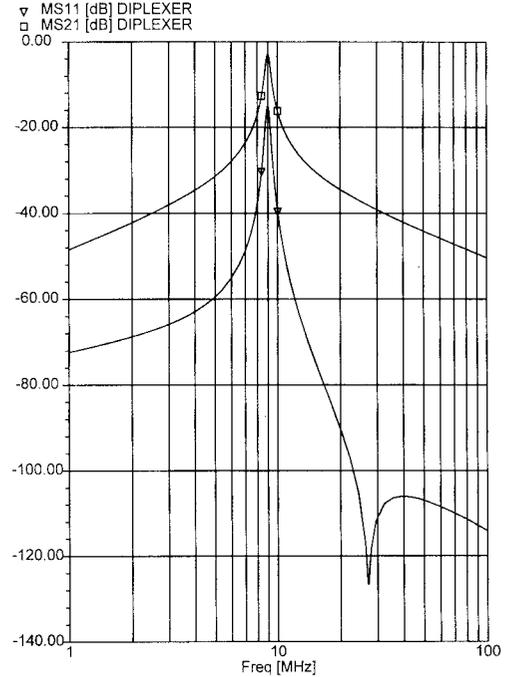


Fig 15.33—The post-mixer amplifier from Hayward and Lawson's *Progressive Communications Receiver* (November 1981 *QST*). This amplifier's gain, including the 6-dB loss of the attenuator pad, is about 16 dB; its noise figure, 4 to 5 dB; its output intercept, 30 dBm. The 6-dB attenuator is essential if a crystal filter follows the amplifier; the pad isolates the amplifier from the filter's highly reactive input impedance. This circuit's input match to 50 Ω below 4 MHz can be improved by replacing 0.01-μF capacitors C1, C2 and C3 with low-inductance 0.1-μF units (chip capacitors are preferable).

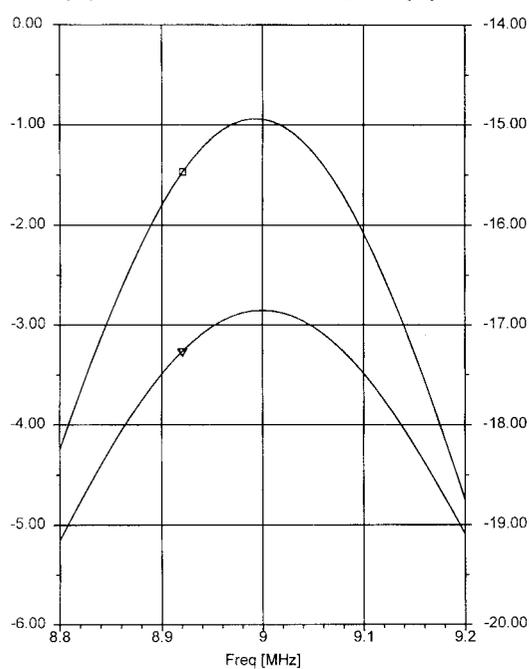
- Q1**—TO-39 CATV-type bipolar transistor, $f_T=1$ GHz or greater. 2N3866, 2N5109, 2SC1252, 2SC1365 or MRF586 suitable. Use a small heat sink on this transistor.
T1—Broadband ferrite transformer, ≈ 42 μH per winding: 10 bifilar turns of #28 enameled wire on an FT 37-43 core.



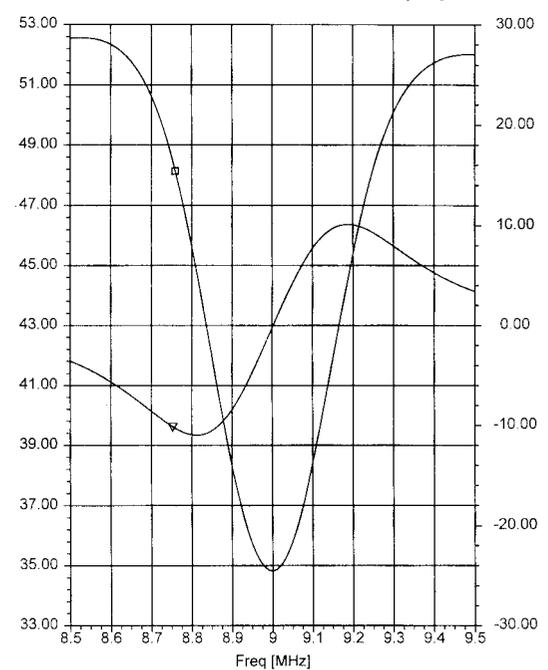
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 Simulation, Input Match (MS11) and Gain (MS21) of 9-MHz Diplexer



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 File: c:\data\word\books\hbk95\mixers\fig15-31.ctk
 Simulation, Diplexer Gain (MS21) and Input Match in 9-MHz Region



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 File: c:\data\word\books\hbk95\mixers\fig15-31.ctk
 Simulation, Real (RZ11) & Imaginary (IZ11) Parts of Diplexer Input Z Near 9 MHz

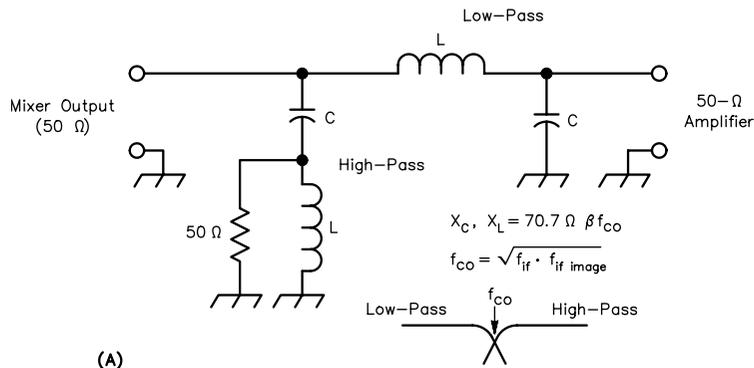


(B)

(C)

(D)

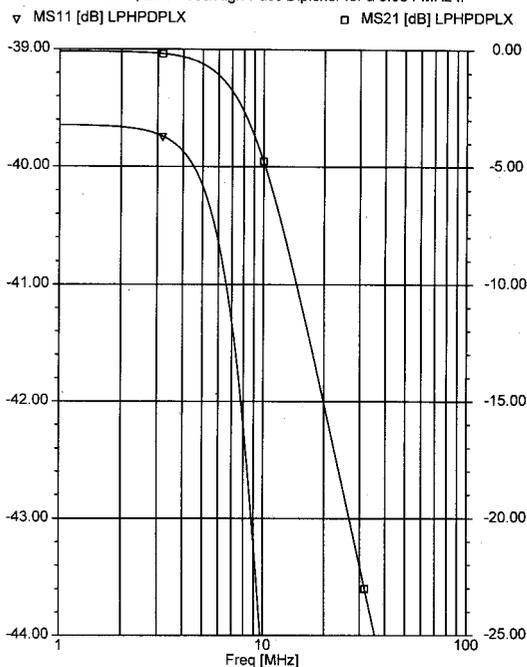
Fig 15.31—A diplexer resistively terminates energy at unwanted frequencies while passing energy at desired frequencies. This band-pass diplexer (A) uses a series-tuned circuit as a selective pass element, while a high-C parallel-tuned circuit keeps the network's terminating resistor R1 from dissipating desired-frequency energy. Computer simulation of the diplexer's response with ARRL Radio Designer 1.0 characterizes the diplexer's insertion loss and good input match from 8.8 to 9.2 MHz (B) and from 1 to 100 MHz (C); and the real and imaginary components of the diplexer's input impedance from 8.8 to 9.2 MHz with a 50-Ω load at the diplexer's output terminal (D). The high-C, low-L nature of the L2-C2 circuit requires that C2 be minimally inductive; a 10,000-pF chip capacitor is recommended. This diplexer was described by Ulrich L. Rohde and T. T. N. Bucher in *Communications Receivers: Principles and Design*.



(A)

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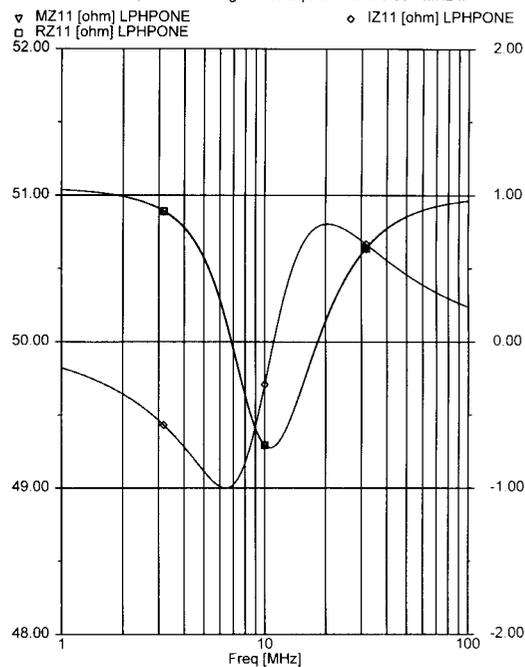
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Simulation, Low-Pass/High-Pass Diplexer for a 3.984-MHz IF



(B)

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Simulation, Low-Pass/High-Pass Diplexer for a 3.984-MHz IF



(C)

Fig 15.32—All of the inductors and capacitors in this high-pass/low-pass diplexer (A) exhibit a reactance of 70.7Ω at its tuned circuits' 3-dB cutoff frequency (the geometric mean of the IF and IF image). B and C show *ARRL Radio Designer* simulations of this circuit configured for use in a receiver that converts 7 MHz to 3.984 MHz using a 10.984-MHz LO. The IF image is at 17.984 MHz, giving a 3-dB cutoff frequency of 8.465 MHz. The inductor values used in the simulation were therefore 1.33 μH ($Q = 200$ at 25.2 MHz); the capacitors, 265 pF ($Q = 1000$). This drawing shows idler load and "50- Ω Amplifier" connections suitable for a receiver in which the IF image falls at a frequency *above* the desired IF. For applications in which the IF image falls below the desired IF, interchange the 50- Ω idler load resistor and the diplexer's "50- Ω Amplifier" connection so the idler load terminates the diplexer low-pass filter and the 50- Ω amplifier terminates the high-pass filter.

SSB-generation process.) **Fig 15.34** shows a more elegant approach to generating full-carrier AM with a DBM.

As we saw earlier when considering the many faces of AM, two DBMs, used in conjunction with carrier and audio phasing, can be used to generate SSB, suppressed-carrier AM. Likewise, two DBMs can be used with RF and LO phasing as an image-reject mixer.

Phase Detection with a DBM

As we saw in our exploration of quadrature detection, applying two signals of equal frequency to a DBM's LO and RF ports produces an IF-port dc output proportional to the cosine of the signals' phase difference (**Fig 15.35**). (This assumes that the DBM has a dc-coupled IF port, of course. If it doesn't—and some DBMs don't—phase-detector operation is out.) Any dc output offset introduces error into this process, so critical phase-detection applications use low-offset DBMs optimized for this service.

Biphase-Shift Keying Modulation with a DBM

Back in our discussion of square-wave mixing, we saw how multiplying a switching mixer's linear input with a square wave causes a 180° phase shift during the negative part of the square wave's cycle. As **Fig 15.36** shows, we can use this effect to produce *biphase-shift keying (BPSK)*, a digital system that conveys data by means of carrier phase reversals. A related system, *quadrature phase-shift keying (QPSK)* uses two DBMs and phasing to convey data by phase-shifting a carrier in 90° increments.

Transistors as Mixer Switching Elements

We've covered diode DBMs in depth because their home-buildability, high performance and suitability for direct connection into 50-Ω systems makes them attractive to Amateur Radio builders. The abundant availability of high-quality manufactured diode mixers at reasonable prices make them excellent candidates for home construction projects. Although diode DBMs are common in telecommunications as a whole, their conversion loss and relatively high LO power requirement have usually driven the manufacturers of high-performance MF/HF Amateur Radio receivers and transceivers to other solutions. Those solutions

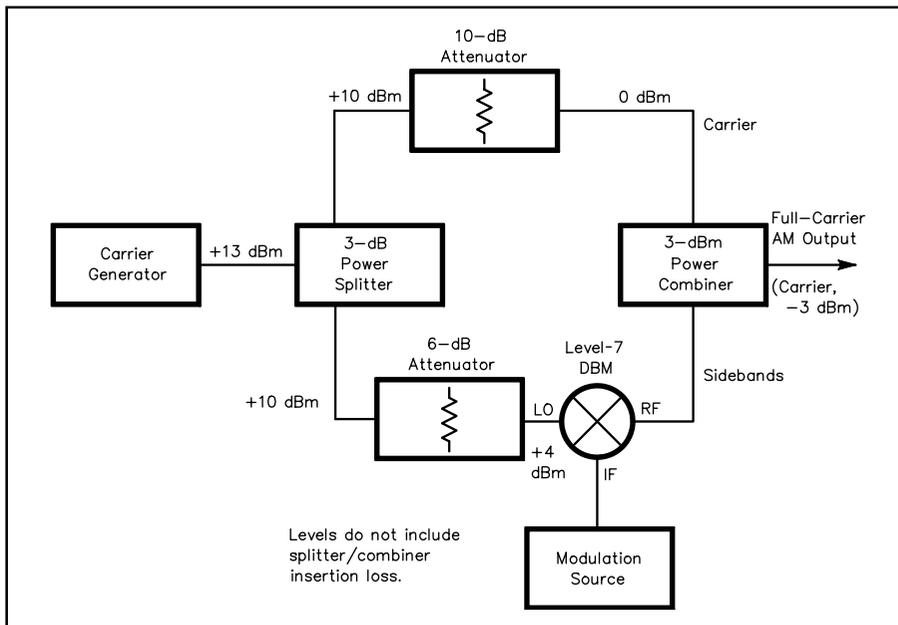


Fig 15.34—Generating full-carrier AM with a diode DBM.

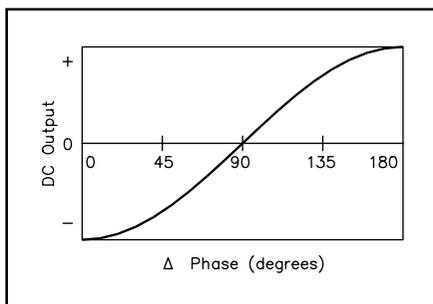


Fig 15.35—A phase detector's dc output is the cosine of the phase difference between its input and reference signals.

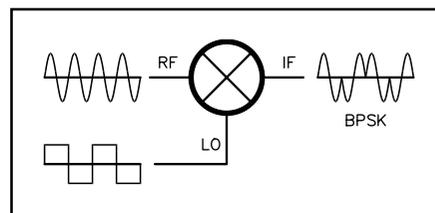


Fig 15.36—Mixing a carrier with a square wave generates biphase-shift keying (BPSK), in which the carrier phase is shifted 180° for data transmission. In practice, as in this drawing, the carrier and data signals are phase-coherent so the mixer switches only at carrier zero crossings.

have generally involved single- or double-balanced FET mixers—MOSFETs in the late 1970s and early 1980s, JFETs from the early 1980s to date. Many of the JFET designs are variations of a single-balanced mixer circuit introduced to *QST* readers in 1970!

Fig 15.37 shows the circuit as it was presented by William Sabin in “The Solid-State Receiver,” *QST*, July 1970. Two 2N4416 JFETs operate in a common-source configuration, with push-pull RF input and parallel LO drive. **Fig 15.38** shows a similar circuit as implemented in the ICOM IC-765 transceiver. In this version, the JFETs (2SK125s) operate in common-gate, with the LO applied across a 220- Ω resistor between the gates and ground.

Bipolar junction transistors can also work well in switching mixers. In June 1994 *QST*, Ulrich Rohde, KA2WEU, published a medium-frequency mixer well-suited to shortwave applications and home-built projects. Shown in **Fig 15.39**, it consists of two transistors in a push-pull, single-balanced configuration. Because of the degenerative feedback introduced by the 20- Ω emitter resistors, the two transistors need not be matched. This mixer’s advantage lies in its achievement of a 33-dBm output intercept with only 17 dBm of local oscillator drive. (Typically, a diode DBM with the same IMD performance requires 25 to 27 dBm of LO drive.) Tests indicate that the upper frequency limit of this mixer lies in the 500-MHz region. The circuit’s lower frequency limit depends on the transformer inductances and the ferrites used for the transformer cores.

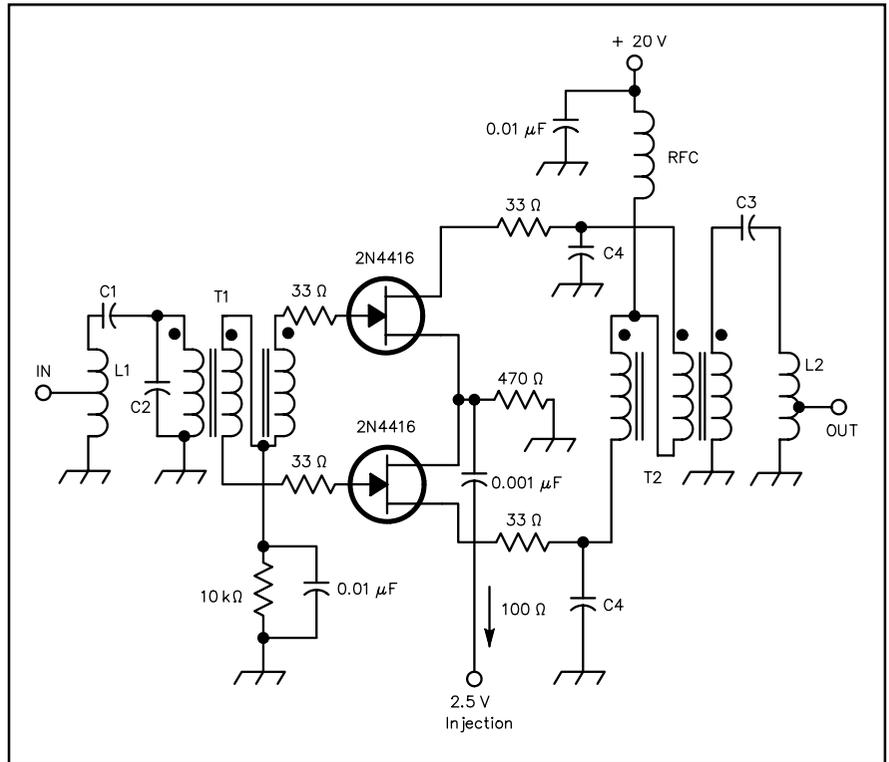


Fig 15.37—Two 2N4416 JFETs provide high dynamic range in this mixer circuit from Sabin 1970. L1, C1 and C2 form the input tuned circuit; L2, C3 and C4 tune the mixer output to the IF. The trifilar input and output transformers are broadband transmission-line types.

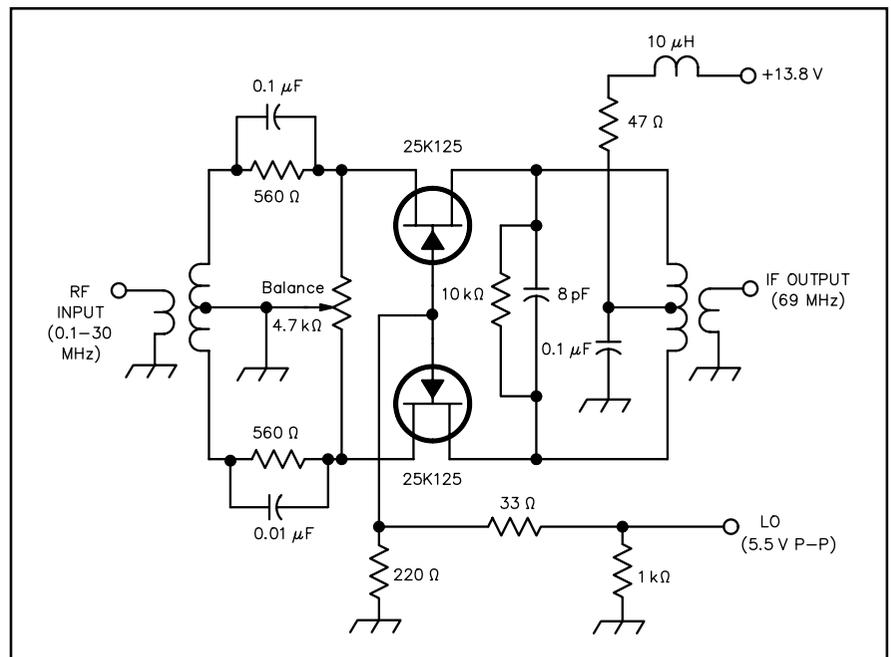


Fig 15.38—The ICOM IC-765’s single-balanced 2SK125 mixer achieves a high dynamic range (per *QST* Product Review, an IP_3 of 10.5 dBm at 14 MHz with preamp off) with arrangement very much like Sabin’s. The first receive mixer in many commercial Amateur Radio transceiver designs of the 1980s and 1990s used a 2SK125 pair in much this way.

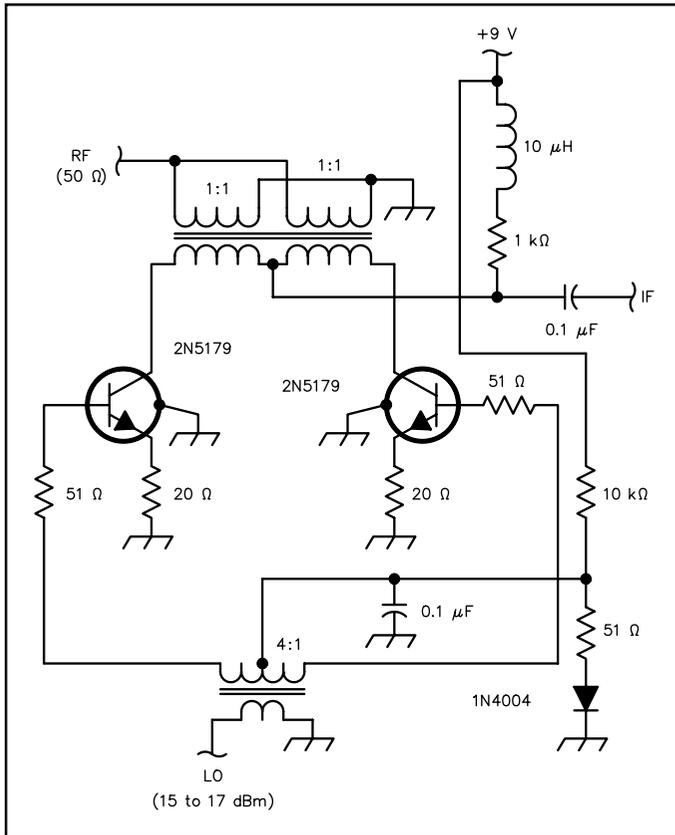


Fig 15.39—This single-balanced, push-pull BJT mixer achieves a high dynamic range ($IP_3 \approx 33$ dBm) with 15 to 17 dBm of LO drive. Its insertion loss is approximately 6 dB. A diode-ring mixer would require 25 to 27 dBm of LO drive to achieve the same IP_3 .

Examining the state of the art in the 1990s, however, we find that the best receive-mixer dynamic ranges are achieved with quads of RF MOSFETs operating as passive switches, with no drain voltage applied. The best of these techniques involves following a receiver's first mixer with a diplexer and low-loss roofing crystal filter, rather than terminating the mixer in a strong wideband amplifier.

A HIGH-DYNAMIC-RANGE MF/HF FRONT END

Jacob Mahkinson, N6NWP, achieved outstanding results with a Calogic SO8901 mixer and CMOS flip-flop. Colin Horrabin, G3SBI, found that a Siliconix SD5000 DMOS FET quad switch driven by a local oscillator signal from an advanced CMOS flip-flop (74AC74N) gave even better performance. His adaptation of Mahkinson's circuit is shown in Figs 15.40, 15.41, 15.42, 15.43 and 15.44. The Siliconix SD5000 (Fig 15.40) serves as the core of this system, which was experimentally built for 14-MHz reception with a 9-MHz IF, and later tested from 1.8 to 30 MHz. Mahkinson's system was described in February 1993 *QST*. Horrabin's experiments have been the subject of an ongoing discussion in the Technical Topics column of The Radio Society of Great Britain's *Radio Communication* since September 1993.

The Mixer

Fig 15.41 shows the mixer/diplexer schematic. T1 matches the 50- Ω source (the preamplifier) to U2's RF port, and T2 matches U2's IF port to a 50- Ω load—the post-mixer amplifier.

In a commutation mixer, there is a compromise between conversion loss and intermodulation distortion. The impedance ratios of transmission-line transformers T1 and T2 are chosen to achieve low intermodulation distortion while keeping conversion loss at an acceptable level.

The LO waveform applied to U2 is of great importance. To achieve a high intercept point, the LO drive must approach an ideal square wave with a 50% duty cycle. Flip-flop U1 divides the LO signal frequency by two and provides 50% duty cycle square waves at its complimentary outputs. The offset bias adjustment potentiometers, R6 and R7, allow compensation for mismatch among the SD5000's MOSFETs and ensure that the commutation mixer switches operate in a 50% duty cycle mode.

The mixer's intercept point also depends on the LO drive level. Zener diode D1 establishes the power supply voltage for U1 and, therefore, the voltage swing at the mixer LO port. In the interest of obtaining a high intercept point, a 9.1-V diode is used at D1.

A band-pass diplexer follows the mixer. It comprises L1, C8, C9, L2, C10, C11, R10 and R11. The 9-MHz signal passes through this network with minimum attenuation, while out-of-passband signals over a wide frequency range are dissipated. Resistor R10 presents a 50- Ω impedance to the mixer, while R11 presents a 50- Ω impedance to the input of the post-mixer amplifier. C7 cancels the inductive reactance of the mixer's input source.

The measured performance of the mixer/diplexer circuit gives input intercepts of at least 42 dBm (HF bands) and 46 dBm (1.8 MHz).

The Preamplifier

The push-pull preamplifier (Fig 15.42) uses MRF586s in a "noiseless feedback" configuration. BIAS controls R12 and R14 allow the collector current of each transistor to be adjusted to the desired value of 25 mA. T3 matches the amplifier input to the 50- Ω band-pass input filter; T4 matches the preamp output to the mixer signal port.

Transformers T5 and T6 couple part of the collector signal back into the emitter (negative feedback), set the gain to 8 dB, and set the input and output impedances to 50 Ω . C12 cancels the inductive reactance of the mixer's source impedance at 14 MHz. (In a multiband receiver,

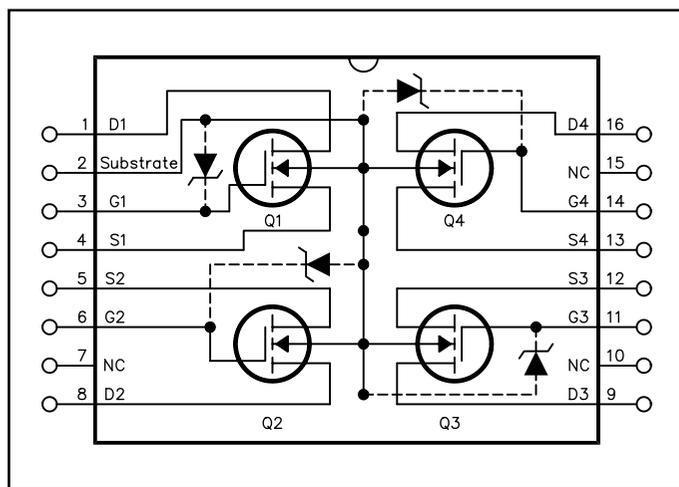


Fig 15.40—The N6NWP/G3SBI front end uses a Siliconix SD5000 DMOS FET quad switch.

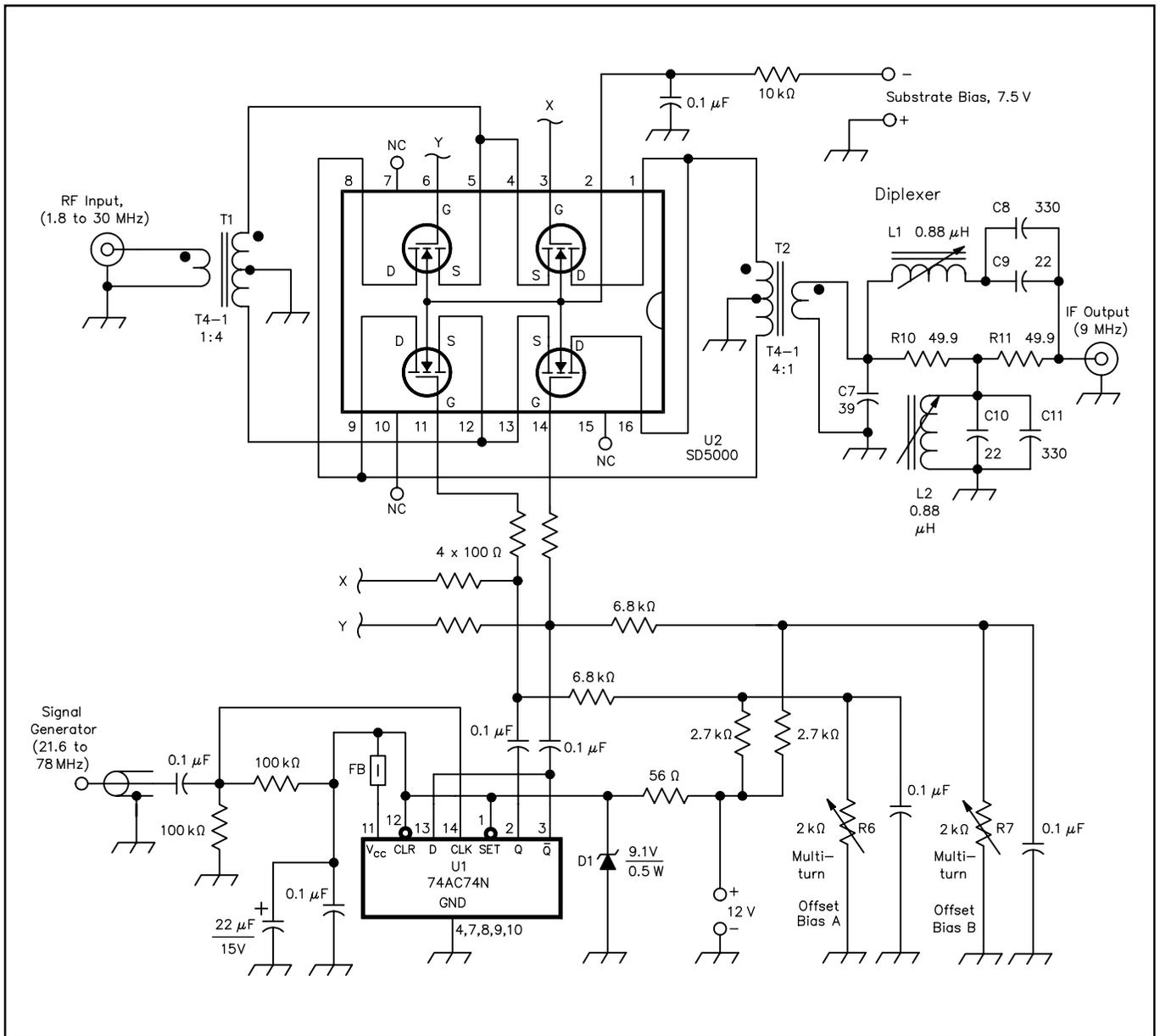


Fig 15.41—Colin Horrabin's DMOS FET quad switching mixer implements Mahkinson's approach with advanced CMOS flip-flops (74AC74Ns operated within their supply ratings) and the Siliconix SD5000 quad DMOS FET. Substrate bias (-7.5 V) is necessary to keep the SD5000's built-in diodes turned off. The ferrite bead on the 74AC74N's V_{CC} pin, and the $100\text{-}\Omega$ resistors in series with the SD5000's gate-drive lines, maximize the mixer's dynamic range by damping ringing, thus keeping the mixer's LO waveform square. The OFFSET BIAS trimmers are set for best mixer balance at a gate-bias level of about 4.5 V .

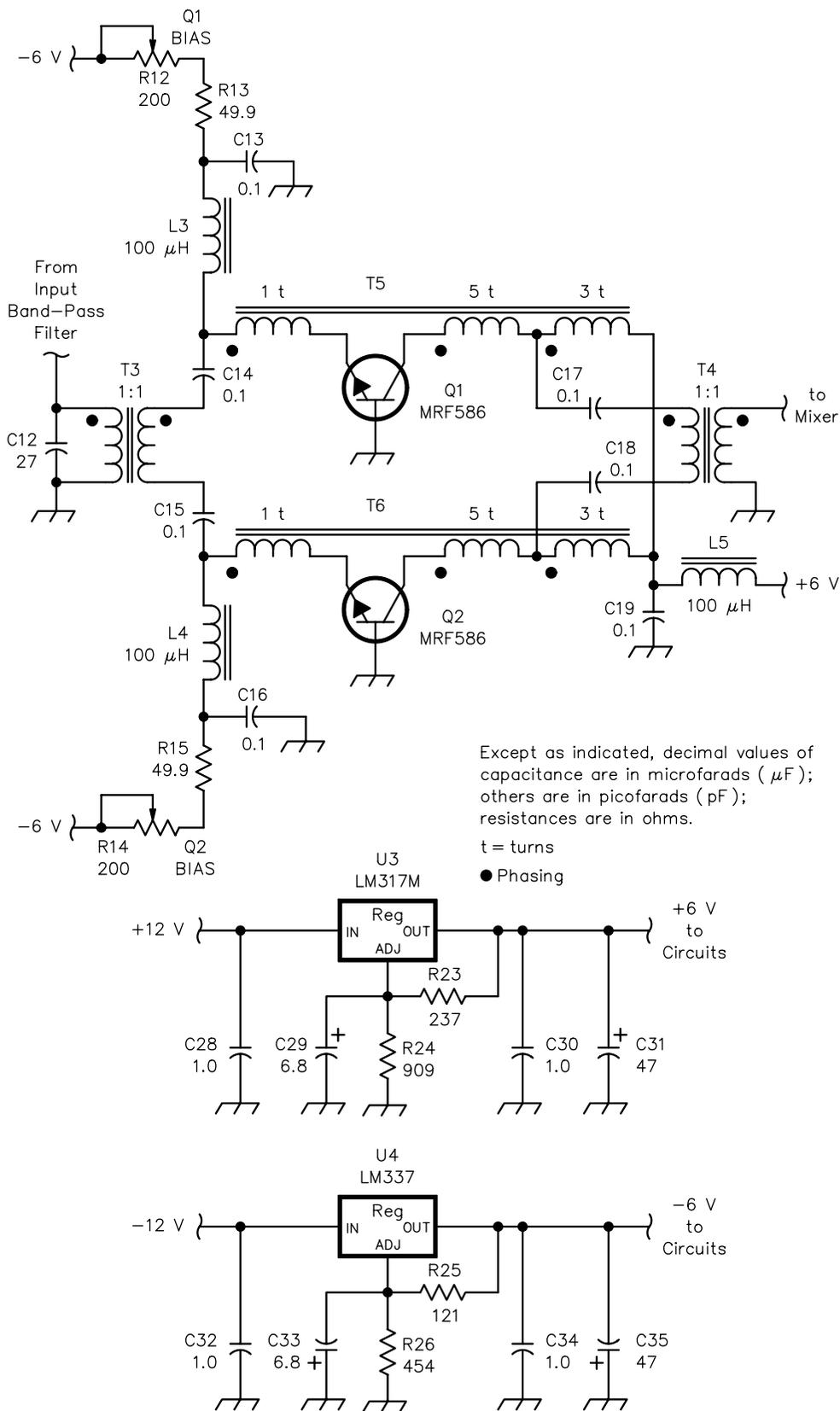


Fig 15.42—The front end's input preamplifier consists of two MRF586 transistors operating in push-pull. The BIAS controls individually adjust the transistors' collector currents to the desired 25 mA. See the parts list sidebar for component information, and Fig 15.44 for how to wind T5 and T6.

this capacitor may be part of switchable preselector filters.) The measured performance of the circuit is: third-order output intercept, 48 dBm; 1-dB output compression point, 25 dBm; gain, 8.0 dB; noise figure, 2.0 dB; -1-dB frequency response 1 to 40 MHz. This performance fully meets the preamplifier design goals.

Post-Mixer Amplifier

The post-mixer amplifier (Fig 15.43) uses the same basic configuration as the preamplifier. This is Colin Horrabin's adaptation of Jacob Mahkinson's original post mixer amplifier. The MRF580A transistors have a collector current bias of 60 mA per device. This helps to overcome the negative effects of load on the post-mixer amplifier's intercept point. T11, a transmission-line transformer, matches the crystal filter's impedance (500 Ω in this case) to the amplifier's 50-Ω output impedance. C20 cancels the inductive reactance of the amplifier's input source. The measured performance of the post-mixer amplifier is: output intercept, +56 dBm; gain, 8.8 dB; and noise figure, 0.5 dB.

More High-Performance Mixer Experiments

Colin Horrabin, G3SBI, continued experimenting with variations of Jacob Mahkinson's original high-performance mixer circuit. This led to the development of a new mixer configuration, called an H mode mixer. This name comes from the signal path through the circuit. (See Fig 15.45A.) Horrabin is a professional scientist/engineer at the Science and Engineering Research Council's Darebury Laboratory, which has supported his investigative work on the H-mode switched-FET

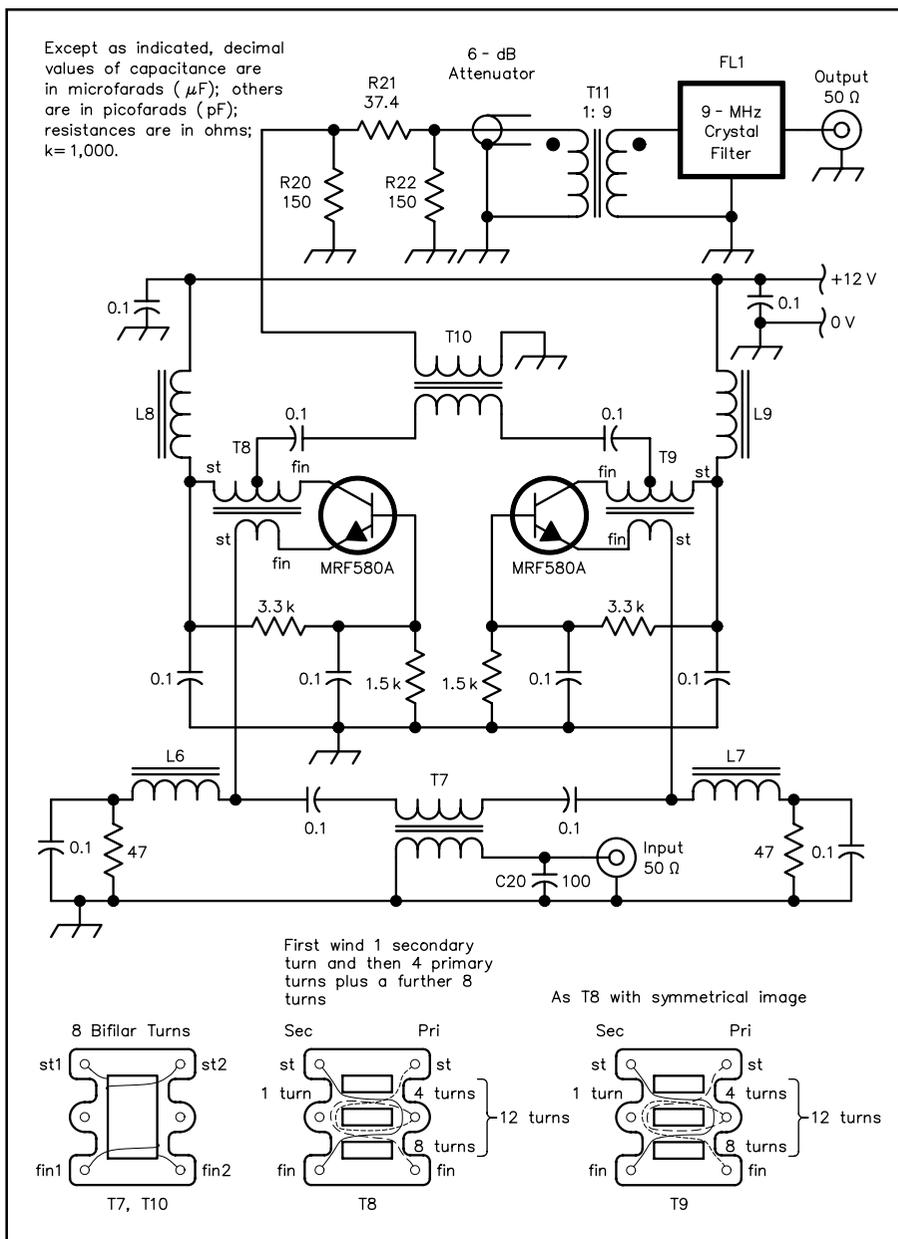


Fig 15.43— G3SBI adapted the N6NWP design to build this post mixer amplifier using MRF580A RF transistors. All resistors are 1/4-W metal-film units. All 0.1 μF capacitors are monolithic ceramic components. L8 and L9 have 4 turns of #28 AWG enameled wire on a ferrite bead. L6, L7, T7, T8, T9 and T10 are wound on Fair-Rite 28-43002402 (material 43) balun cores. L6 and L7 are 6 turns of #28 AWG enameled wire. T7 to T10 use #36 AWG enameled wire. Take two glass fiber Cambion 14-pin DIP sockets, cut each in half and bend the leads out at 90°. Mount the balun cores to a socket half with double sided tape and then wind the transformers as shown. The amplifier was constructed using ground-plane construction.

Parts List for the N6NWP High-Dynamic Range Front End

Unless indicated otherwise, all of the circuits' resistors are $\frac{1}{4}$ -W 1% tolerance, and all of the capacitors are 20%-tolerance ceramic. C7-C12 and C20 are dipped mica, polyester film or epoxy-coated ceramic.

U1—74HAC74N CMOS flip-flop.

U2—Siliconix SD5000 DMOS FET Quad switch.

U3—LM317MP adjustable voltage regulator IC.

U4—LM337MP adjustable voltage regulator IC.

Q1-Q2—Motorola MRF586 RF transistor.

Q3-Q4—Motorola MRF580A RF transistor. Use heat sink (Thermalloy 2228B or Aavid 3257).

T1, T2—Mini-Circuits T4-1 4:1 RF transformer.

T3, T4—Mini-Circuits T1-1 1:1 RF transformer.

T11—Mini-Circuits T9-1 9:1 RF transformer.

L3-L5—100-mH RF choke.

L1, L2—Toko 332PN-T1012Z (Digi-Key TK5130) 1.0-mH variable inductor.

R6, R7—2-k Ω multiturn potentiometer.

R12, R14—200- Ω multiturn potentiometer.

D1—9.1-V, 0.5-W Zener diode,

1N5239 suitable. See text.

C7—39 pF $\pm 10\%$.

C8, C10—330 pF $\pm 5\%$.

C9, C11—22 pF $\pm 5\%$.

C12—27 pF $\pm 10\%$.

C20—100 pF $\pm 10\%$.

T5, T6—homemade trifilar transformer wound with #32 enameled wire on Amidon BN-43-2402 core (Fair-Rite 2843002402) as per Fig 15.44.

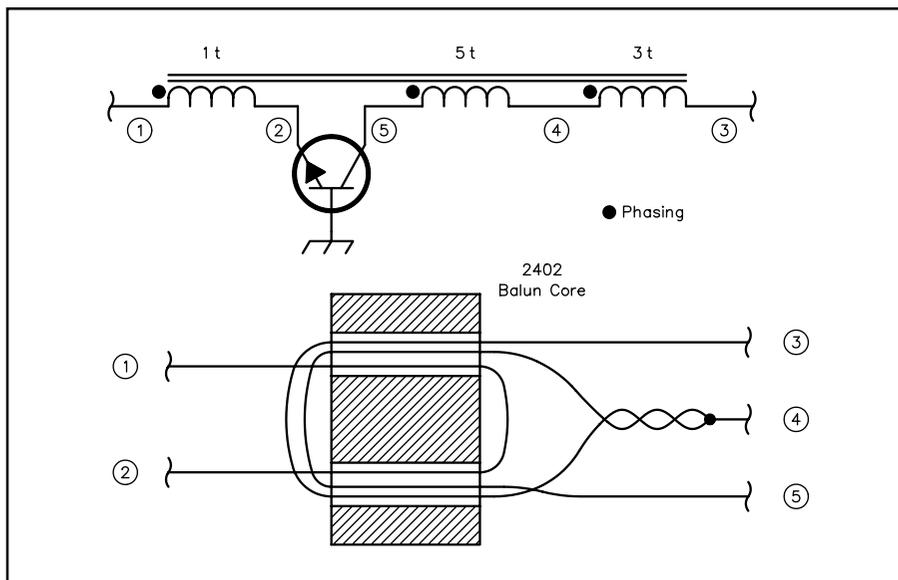


Fig 15.44—Transformers T5 and T6 are wound with #32 wire on two-hole ferrite cores (see the parts list sidebar for core information). Wind the 1-turn winding (1-2) first, the 3-turn winding (4-3) second and the 5-turn winding (5-4) third.

mixer, and consequently holds intellectual title to the new mixer. This does not prevent readers from taking the development further or using the information presented here.

Inputs A and B are complementary square-wave inputs derived from the sine-wave local oscillator at twice the required square-wave frequency. If A is ON, then FETs F1 and F3 are ON and F2 and F4 are OFF. The direction of the RF signal across T1 is given by the E arrows. When B is ON, FETs F2 and F4 are ON and F1 and F3 are OFF. The direction of the RF signal across T1 reverses, as shown by the F arrows.

This is still the action of a switching mixer, but now the source terminal of each FET switch is grounded, so that the RF signal switched by the FET cannot modulate the gate voltage. In this configuration the transformers are important: T1 is a Mini-Circuits type T4-1 and T2 is a pair of these same transformers with their primaries connected in parallel.

The Ubiquitous NE602: A Popular Gilbert Cell Mixer

Introduced as the NE602 in the mid-1980s, the Philips Components-Signetics NE602A mixer-oscillator IC has become greatly popular with amateur experimenters for transmit mixers, receive mixers and balanced modulators. **Fig 15.46** shows its equivalent circuit. The NE602A's typical current drain is 2.4 mA; its supply voltage range is 4.5 to 8.0.

As we learned in exploring sine-wave versus square-wave mixing, the NE602's mixer is a Gilbert cell multiplier. Its inputs (RF) and outputs (IF) can be single- or double-ended (balanced) according to design requirements (**Fig 15.47**). Each input's equivalent ac impedance is approximately 1.5 k Ω in parallel with 3 pF; each output's resistance is 1.5 k Ω . The mixer can typically handle signals up to 500 MHz. At 45 MHz, its noise figure is typically 5.0 dB; its typical conversion gain, 18 dB. Considering the NE602A's low current drain, its input IP₃ (measured at 45 MHz with 60-kHz spacing) is usefully good at -15 dBm. Factoring in the mixer's conversion gain results in an equivalent output IP₃ of about 5 dBm.

The NE602A's on-board oscillator can operate up to 200 MHz in LC and crystal-controlled configurations (**Fig 15.48** shows three possibilities). Alternatively, energy from an external LO can be applied to the chip's pin 6 via a dc blocking capacitor. At least 200 mV P-P of external LO drive is required for proper mixer operation.

NE602A Usage Notes

The '602 was intended to be used as the second mixer in double-conversion FM cellular radios, in which the first IF is typically 45 MHz, and the second IF is typically 455 kHz. Such a receiver's second mixer can be relatively weak in terms of dynamic range because of the adjacent-signal protection afforded by the high selectivity of the first-IF filter preceding it. When used as a first

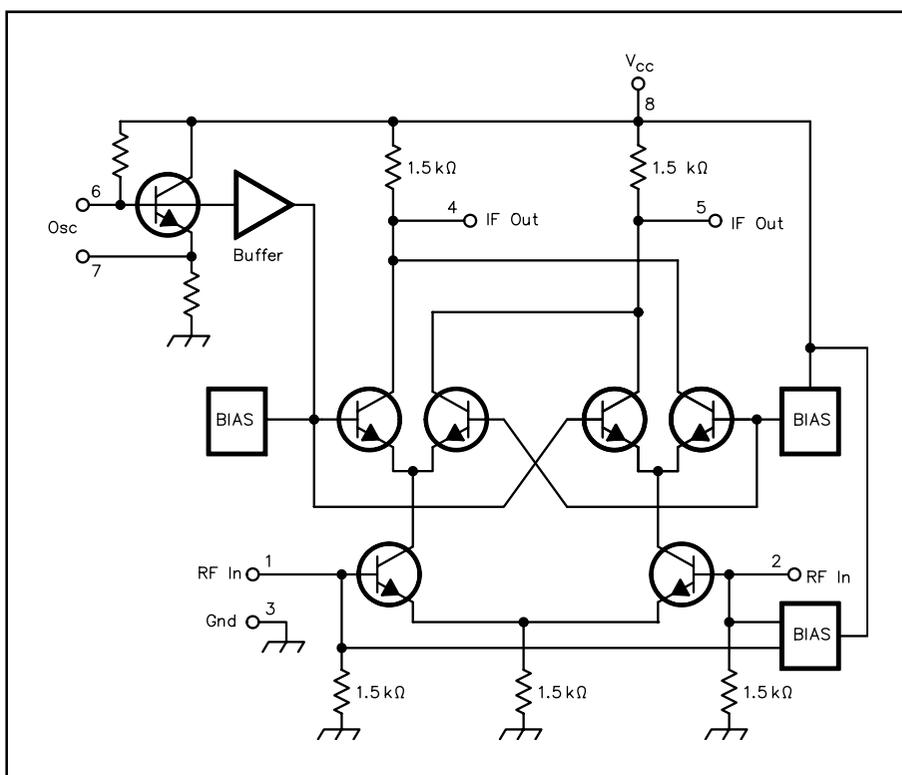


Fig 15.46—The NE602A's equivalent circuit reveals its Gilbert-cell heritage.

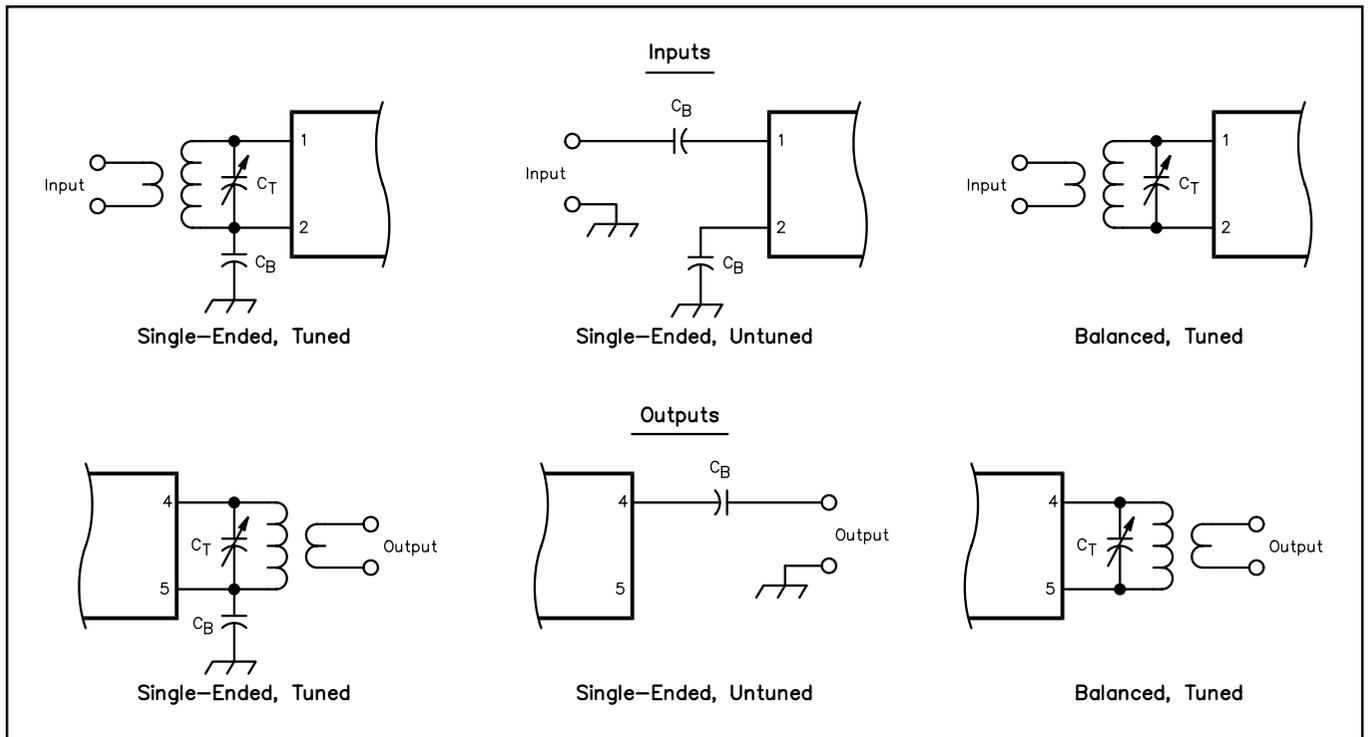


Fig 15.47—The NE602A’s inputs and outputs can be single- or double-ended (balanced). The balanced configurations minimize second-order IMD and harmonic distortion, and unwanted envelope detection in direct-conversion service. C_T tunes its inductor to resonance; C_B is a bypass or dc-blocking capacitor. The arrangements pictured don’t show all the possible input/output configurations; for instance, you can also use a center-tapped broadband transformer to achieve a balanced, untuned input or output.

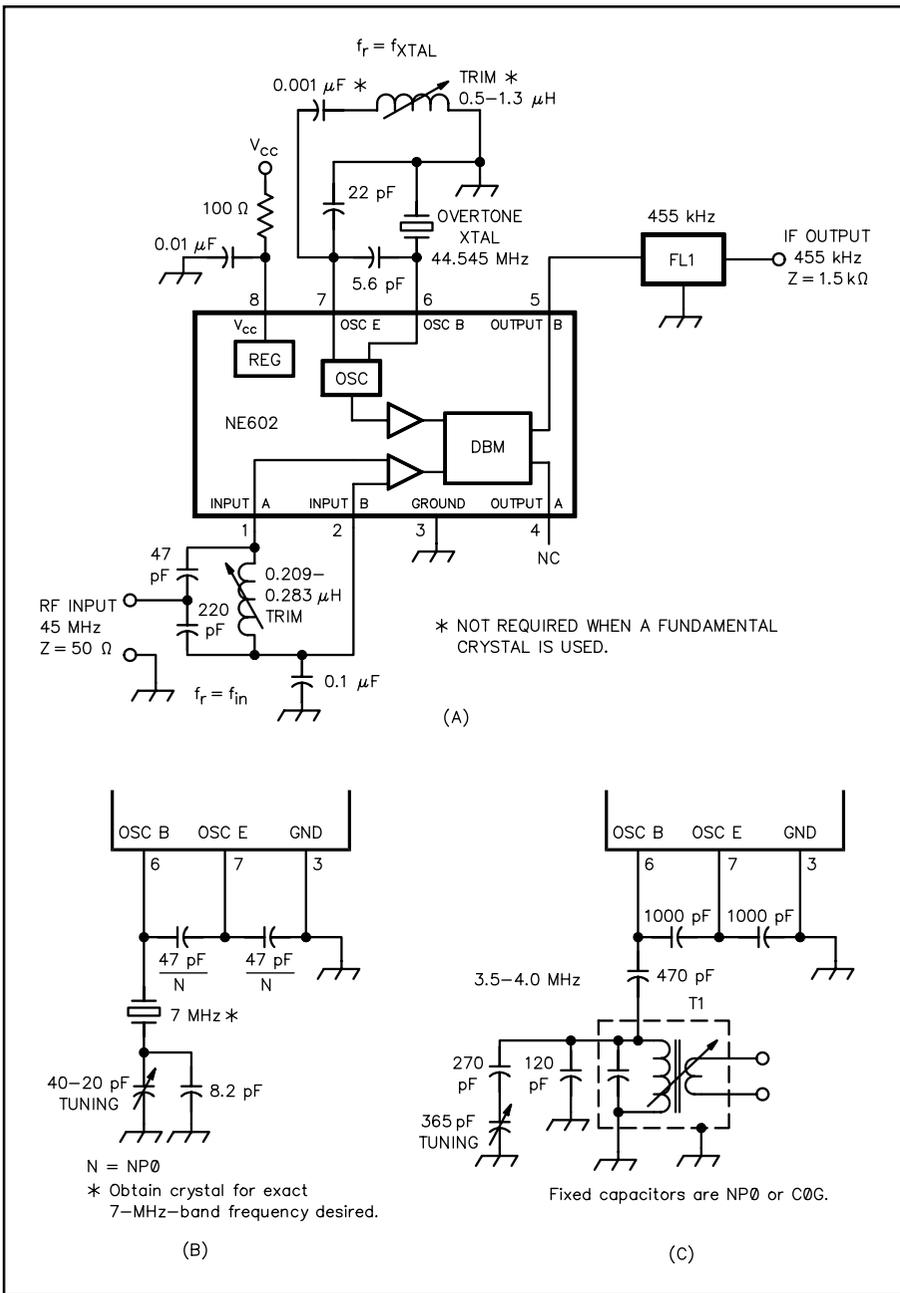


Fig 15.48—Three NE602A oscillator configurations: crystal overtone (A); crystal fundamental (B); and LC-controlled (C). T1 in C is a Mouser 10.7-MHz IF transformer, green core, 7:1 turns ratio, part no. 421F123.

a result of wider tolerances. All of these parts should nonetheless work satisfactorily in most “NE602” experimenter projects. The same mixer/oscillator topology, modified for slightly higher dynamic range at the expense of somewhat less mixer gain, is also available in the Philips Components-Signetics mixer/oscillator/FM IF chips NE/SA605 (input IP₃, typically –10 dBm) and NE/SA615 (input IP₃, typically –13 dBm).

mixer, the ’602 can provide a two-tone third-order dynamic range between 80 and 90 dB, but this figure is greatly diminished if a pre-amplifier is used ahead of the ’602 to improve the system’s noise figure.

When the ’602 is used as a second mixer, the sum of the gains preceding it should not exceed about 10 dB. NE602 product detection therefore should not follow a high-gain IF section unless appropriate attenuation is inserted between the ’602 and the IF strip.

The ’602 is generally *not* a good choice for VHF and higher-frequency mixers because of its input noise and diminishing IMD performance at high frequencies. There are applications, however, where 6-dB noise figures and 60- to 70-dB dynamic range performance is adequate. If your target specifications exceed these numbers, you should consider other mixers at VHF and up.

NE602A Relatives

The NE602A (SA602A for operation over a wider temperature range) began life as the NE602/SA602, a part with a slightly lower IP₃ than the A version. The pinout-identical NE612A/SA612A costs less as

A SYNCHRONOUS AM DETECTOR FOR 455 KHZ

Much like switching a receiver or transceiver to SSB and receiving AM as SSB, its carrier at zero beat, synchronous detection overcomes fading-related distortion by supplying an unfading carrier at the receiver. The difference between synchronous AM detection and normal SSB product detection is that synchronous detection *phase locks* its carrier to that of the incoming signal. No tuning error occurs if the received signal happens to fall between a synthesized receiver's tuning steps, and the detector's PLL compensates for modest tuning drift. The result is a dramatic fidelity improvement over diode-detected AM. This circuit, designed by Jukka Vermasvuori, OH2GF, and originally published in July 1993 *QST*, requires only a digital voltmeter for alignment.

The Synchronous Detector Circuit

See **Fig 15.49**. The unit uses popular NE602AN (mixer/oscillator) and NE604AN (FM subsystem) ICs to provide both synchronous and quasi-synchronous detection. (This discussion refers to U1 and U2 as NE602ANs, but NE602Ns, SA602Ns and SA602ANs will work equally well in this application. Likewise, an NE604N, NE604AN, SA604N or SA604AN will work well at U3 in this application.) Operating at a supply voltage of 6, the circuit draws 10 mA. U1, an NE602AN, acts as the BFO and product detector necessary for synchronous detection. Feeding U1's balanced inputs in push-pull helps keep BFO energy from backing out of the input pins and into U3's limiting circuitry. To take advantage of the chip's internal biasing, the input transformer (T1) is isolated with dc blocking capacitors. (U1 also supplies balanced audio output, but usefully reducing this to a single-ended output would have required an operational amplifier. Doing so would reduce even-harmonic distortion in recovered audio, but would not justify the increased circuit complexity and power consumption.)

U1's oscillator amplitude is optimized to 660 mV P-P (as measured across L1) by the 220- Ω resistor at the oscillator output at pin 7. The BFO frequency is adjusted by two variable-capacitance diodes (D1 and D2) in addition to the tank coil, L1. D2 receives its control voltage via switch S3 (BFO MODE), which selects control voltage from either U3's phase detector (SYNC) or a constant voltage from a resistive divider (CW/SSB/TUNE).

The fixed CW/SSB/TUNE voltage (2.16, set by the ratio of R5 and R6) corresponds to the phase detector's optimum output voltage at lock.

R2, BFO TUNING, drives D1 to provide manual detector tuning without upsetting the D2's control-voltage optimization. S2, BFO OFFSET, presets R2's tuning range into the optimum regions for LSB (-2 kHz), DSB AM (± 0 kHz) and USB ($+2$ kHz). (These offsets are for a radio in which received sidebands

Fig 15.49—The synchronous detector operates in the 450- to 455-kHz region. Except as otherwise specified, its fixed-value resistors are $1/4$ -W, 5%-tolerance units, and its capacitors' working voltages can be 10 or higher. See the **References** chapter for a list of part suppliers addresses that includes addresses of the firms mentioned below.

D1, D2—BB809 or BB409 tuning diode. Each of these, a "28-V" diode, exhibits approximately 33 pF at 2 V and an unusually high voltage-versus-capacitance slope of 10. Two paralleled 30-V Motorola tuning diodes (one MV2109 [≈ 45 pF at 2 V] and one MV2105 [≈ 18 pF at 2 V], both with a slope of 3) may serve as a substitute for each BB809 or BB409.

L1—Approximately 215 μ H. Toko RWRS-T1019Z (nominally 220 μ H, Q of 100 at 796 kHz, available as Digi-Key Corp no. TK1223), suitable.

R1—50-k Ω trimmer.

R2—10-k Ω linear control.

T1—13 trifilar turns of #28 enameled wire, twisted, on an FT-37-77 toroidal ferrite core.

U1, U2—Signetics NE602N, NE602AN, SA602N, SA602AN mixer/oscillator IC.

U3—Signetics NE604N, NE604AN, SA604N or SA604AN FM receiver subsystem IC.

Quasi-Synchronous Detection

Synchronous detection can be mimicked by amplifying and limiting the AM signal sufficiently (at IF) so that only carrier remains, and substituting this signal for the BFO at the product detector. This *quasi-synchronous detection* acts much like envelope (“diode”) detection and works best when the received signal does not fall to zero, as can often occur with SSB and, with AM, during fading. As the signal fades and the carrier-to-noise (C/N) ratio decreases, noise renders the detector’s switching action inconsistent, and detection quality deteriorates rapidly. Thus, under conditions of low C/N ratio, quasi-synchronous detection exhibits a distinct detection threshold, as does a diode detector. The chief advantage of quasi-synchronous detection over simple diode rectification is its much lower threshold compared to a diode. The detector circuit presented in [Fig 15.49](#) includes a quasi-synchronous detector for flexibility and A/B comparison with the synchronous circuit.

—*Jukka Vermasvuori, OH2GF*

are inverted relative to their on-air sense.) The BFO TUNING control therefore provides fine adjustment for detector lock around coarse receiver tuning steps (1 kHz in the Sangean AT-808 with which this circuit was originally used).

U3’s phase detector requires a 90° phase shift between the incoming and reference phases to give the correct zero-phase output (again, approximately 2.16 V). The all-pass stage, Q1, operates as an isolation stage and adjustable phase shifter to generate the 90° phase shift.

U3 is an NE604N FM IF subsystem IC that contains limiting-amplifier stages (total gain, 101 dB) and a quadrature detector. Band limiting can be inserted between the limiter stages, but experiments with various RC and LC filters brought no improvement, and instead led to increased delay that upset the carrier/sideband phase relationships necessary for good quasi-synchronous detection. The [Fig 15.49](#) circuit uses U3’s quadrature detector as a phase detector that outputs control voltage for D2.

The most difficult aspect of the phase-locking chain is the selection of a time constant for the locking loop. Signal fading, in conjunction the relative absence or presence of phase-modulation components in the transmitted signal, play important roles in detector lock. Were fading not a problem, a short time constant—one allowing fast locking—would suffice for DSB AM. For SSB AM with carrier (which includes a PM component at all modulation frequencies), however, and DSB AM with fading (during which a fast PLL may unlock on the sudden phase shifts that can accompany fast, deep carrier fades), a long time constant is necessary. Particularly for SSB with carrier, the loop bandwidth must be reduced to below the lowest expected modulation frequency. C1 and R4 set the PLL time constant in [Fig 15.49](#).

The received signal strength indicator (RSSI) output at pin 5 of U3 follows the input level logarithmically, giving an output of 1.1 V on noise only (RF INPUT shorted in [Fig 15.49](#)) and 3.3 V at an RF INPUT level of 3 mV. The RSSI output is adaptable as an AGC-detector output, making the NE604AN attractive for simple IF-AGC designs. Because of the NE604AN’s high gain, circuit layout can be critical and requires short leads and physically small bypass capacitors. Coupling must be minimized between pin 9 of U3 and the U1 oscillator components.

U2, an NE602AN, operates as a quasi-synchronous detector. The BFO energy it requires is readily available as a square wave at pin 9 of U3. Except for the fact that its BFO input is derived from limited input signal instead of a VCO, U2 functions the same way as U1.

Construction

Two evaluation models were constructed using ground-plane construction, mounting the ICs upside down and soldering their ground pins directly to ground with minimal lead length. The later version is constructed onto a long and narrow piece of circuit board intended to be the bottom plate of an add-on

box to be fixed underside of a Sangean ATS-808 receiver. (**Fig 15.50** shows this version's general layout.)

With the circuitry arranged per Fig 15.50 and receiver-detector IF-AF connections made with small-diameter coaxial cable to avoid crosstalk, BFO-signal leakage is unmeasurable at U3; that is, the voltage at RSSI does not change when the BFO is temporarily disabled under no-signal conditions.

Obtaining IF Drive

A simple BJT emitter follower (**Fig 15.51**) can connect the synchronous detector to a solid-state transceiver. The detector can also be driven from the unbypassed cathode resistor of a vacuum-tube receiver's final IF stage (Fig 15.51B). If test equipment is not available to allow accurate measurement of the detector's input level, just keep the detector drive comfortably below that at which distortion begins.

IF signal can be obtained from the ATS-808 receiver via a 56-pF capacitor connected to the hot end of the '808's transformer T9 (at pin 16 of the '808's U1, a TA7758P IC). Connecting the detector cable detunes T9, which, though difficult to reach, must be retuned by turning its slug outwards a few turns to obtain maximum audio output. The detector's audio output (AF OUT) returns to the AT-808 by means of the '808's TONE switch, which can be rewired to select audio from the outboard detector.

The key to success with this

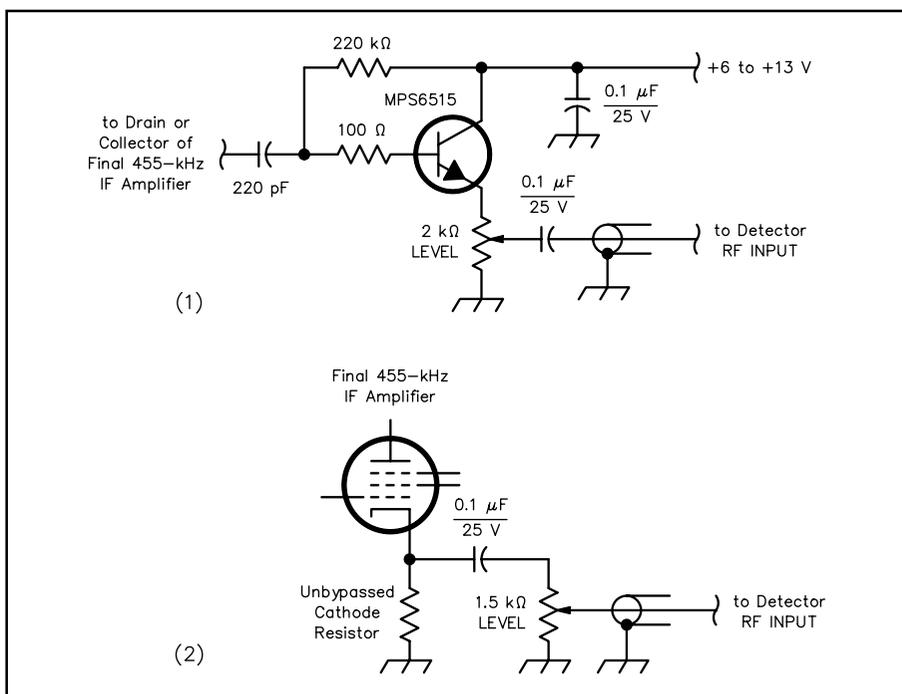


Fig 15.51—Simple circuitry can connect the synchronous detector to a solid-state transceiver (1) or a vacuum-tube receiver (2) if a 455-kHz output tap is not already available.

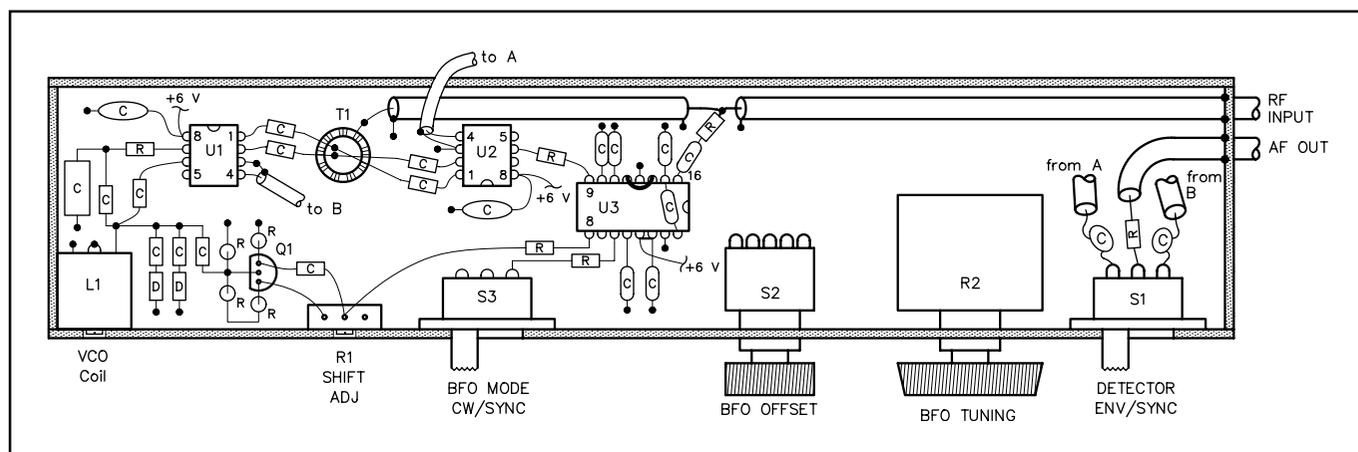


Fig 15.50—One recommended layout for the synchronous detector. U3's high gain requires care in construction—see text.

circuit is getting *interference-free* IF drive. U3's RSSI output can be of critical importance in scoping out possible BFO leakage and/or unwanted signal input. With the detector's RF input shorted, a voltmeter connected to RSSI should indicate about 1.1. If it doesn't, U1's BFO signal may be getting into U3. The RSSI indication shouldn't be much higher than this with the detector connected to the receiver, with the receiver's RF gain control turned all the way down for minimal noise input to the detector. (The receiver used must be in "AM" mode—BFO *off*.) If the RSSI voltage is above the 1-V range at this point, a receiver oscillator or some other signal, however inaudible, may be driving U3's limiter. *The detector will be unable to achieve and hold lock if anything other than the receiver IF signal captures its limiter.* The NE604's limiter stages, specified to work up to at least 21 MHz, are capable of 101 dB of overall gain and specified to be several decibels into limiting with *as little as 3 μ V* (−92 dBm) applied across a 50- Ω load at the '604's input!

Operation

After checking the circuit, connect it to a 6-V power supply. The total current consumption should be approximately 10 mA. Switch the DETECTOR switch to ENVELOPE; band noise should now be audible. Tune in a strong AM signal, switch S3, BFO MODE, to SSB/CW/TUNE, and set the DETECTOR switch to SYNC. The detector may sound very quiet at this point. Adjust L1's core until the signal swoops into audibility. This proves that the BFO is oscillating. If possible, measure the BFO level across L1 with an oscilloscope and 10:1 probe; it should be about 660 mV. (If test-equipment unavailability disallows this measurement, go to the next paragraph.) If it's not, experiment with R3's value to make it so.

Accurately tune the receiver to a strong, pure carrier, such as a beacon. Adjust R2, BFO TUNING, for a wiper voltage of 2.00 with S2, BFO OFFSET, set to ± 0 kHz. Mark as CENTER this point in its knob's travel. With the BFO MODE, switch in the SSB/CW/TUNING position, use a nonmetallic tool to adjust L1, the VCO coil, for zero beat with the incoming carrier. Returning S3 to the SYNC position should allow carrier lock if R1, SHIFT ADJ, is reasonably near adjustment. Adjust SHIFT ADJ for carrier lock if necessary. This completes coarse adjustment of SHIFT ADJ. Return the BFO MODE switch to the SSB/CW/TUNING position; the BFO should still be at or very close to zero beat with the incoming signal.

Return the BFO MODE switch to the SYNC position. After the detector locks, fine-tune SHIFT ADJ to minimize detected low-frequency hiss. (If a sufficiently strong unmodulated local signal is not available off-air, transmit into a dummy antenna with a PLL-synthesized transceiver and make this adjustment by listening to its signal. It should be possible to find a SHIFT ADJ setting at which detected hiss distinctly nulls. As a less desirable alternative, tune in an unfading AM signal modulated with a 1-kHz tone and adjust SHIFT ADJ for maximum tone recovery.) Once this is done, the detector's carrier phase is within exactly 0° or 180° of the BFO signal applied to the amplitude detector (U1), and the detector's response to phase noise has been minimized. Also significantly, this alignment procedure sets the detector to lock in a range centered on the control voltage that corresponds to optimum locking sensitivity *and* minimum phase noise demodulation. This completes alignment.

To zero-beat and lock a given station: Set the BFO TUNING control to its center (2.00 V) position, BFO MODE switch to CW/SSB/TUNE and BFO OFFSET to match the sideband(s)—LSB, USB or both—desired. Tune the receiver as close to zero beat as its tuning steps allow. Adjust BFO TUNING for zero beat. Switch the BFO MODE switch to SYNC to lock the detector.

Toggling S1, DETECTOR, between ENVELOPE and SYNC allows easy comparison of the effects of detection mode under adverse propagation conditions. The synchronous mode will be considerably superior much of the time. The quasi-synchronous (ENVELOPE) mode may give crisper audio under average or poor signal conditions; this effect may be due to increasing distortion as the signal approaches the noise floor, however.

Properly adjusted, the synchronous detector operates at less than 1% total harmonic distortion. (The

quasi-synchronous detector provides comparable performance—but only on a nonfading test signal.) Measurements confirm the importance of setting R1, SHIFT ADJ, properly: Improper adjustment can increase low-order harmonic-distortion products by 3 to 12 dB in addition to increasing the detector's sensitivity to phase noise.

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